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AMATH 483/583 High Performance Scientific Computing

Lecture 11: Threads, Shared Memory Parallelism

Andrew Lumsdaine Northwest Institute for Advanced Computing Pacific Northwest National Laboratory University of Washington Seattle, WA

Overview

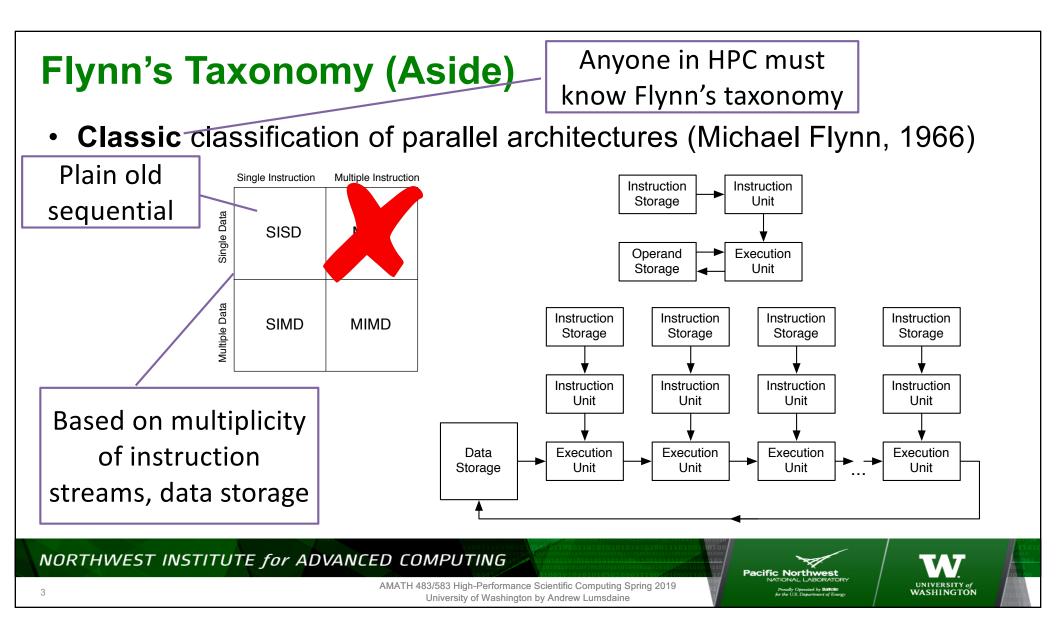
- Multiple cores
- Threads
- Parallelization strategies
- Correctness
- Performance

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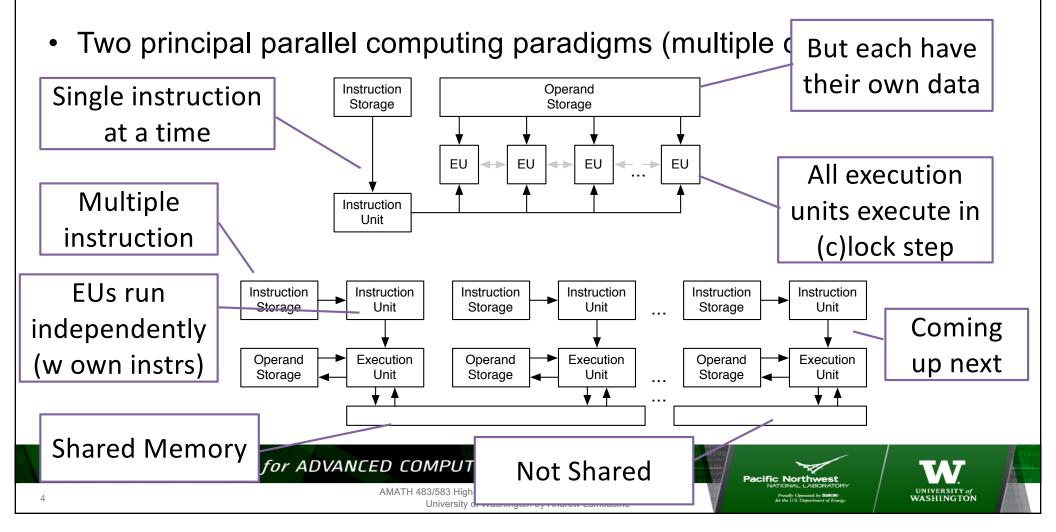


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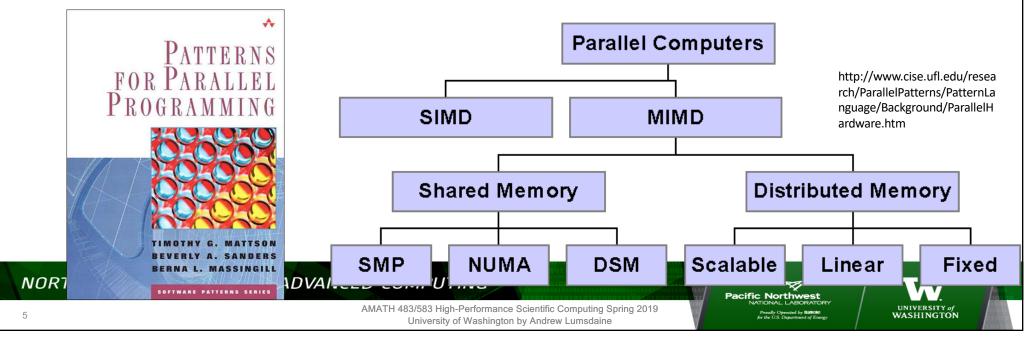


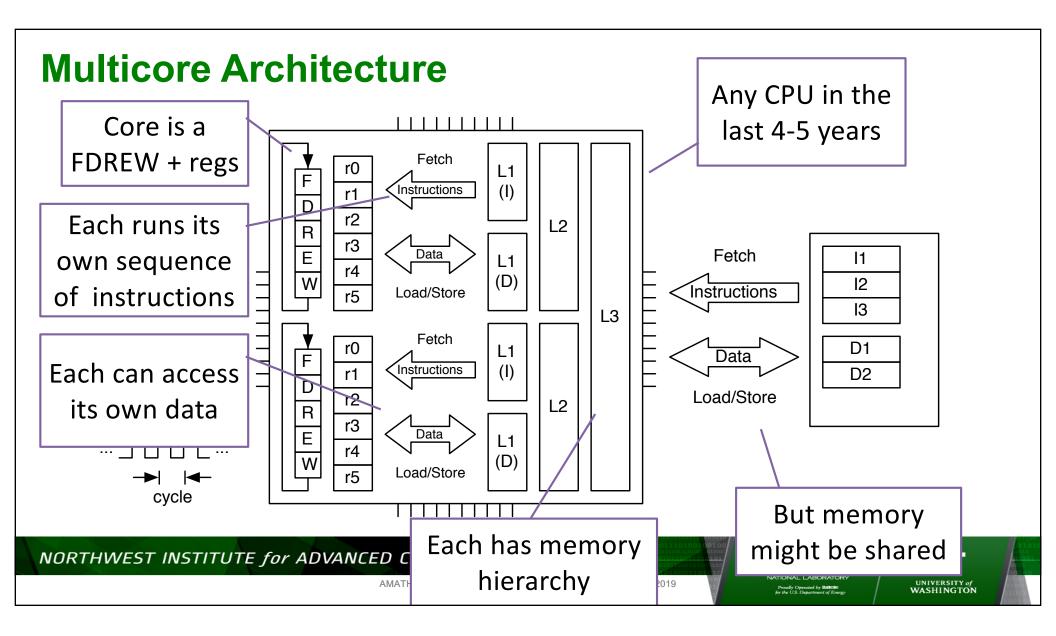
SIMD and MIMD

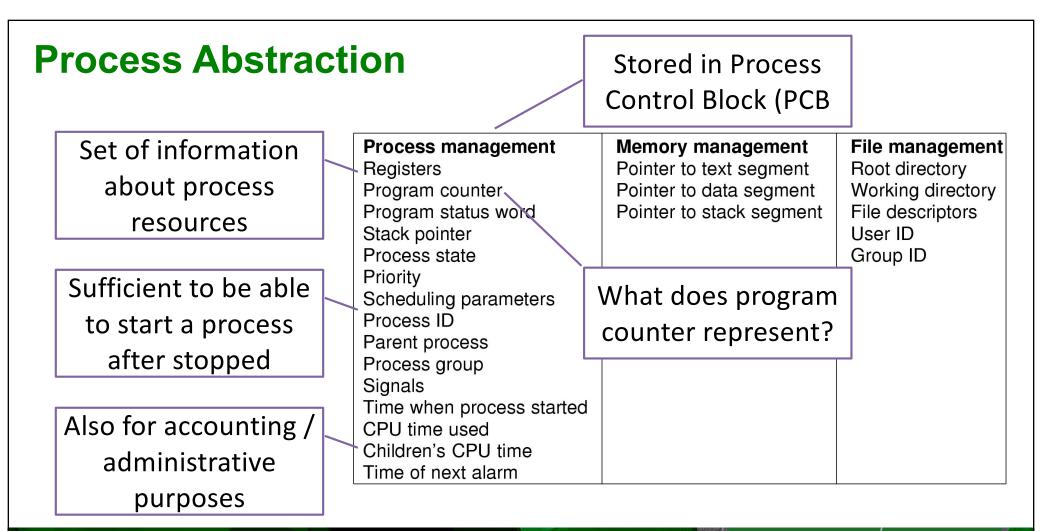


A More Refined (Programmer-Oriented) Taxonomy

- Three major modes: SIMD, Shared Memory, Distributed Memory
- Different programming approaches are generally associated with different modes of parallelism (threads for shared, MPI for distributed)
- A modern supercomputer will have all three major modes present





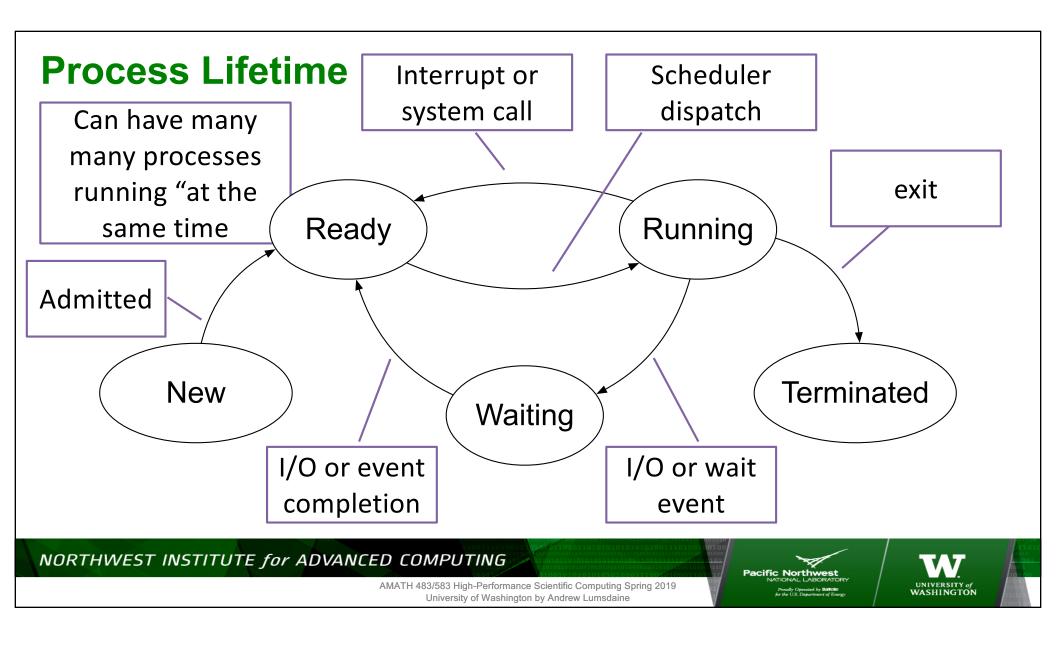


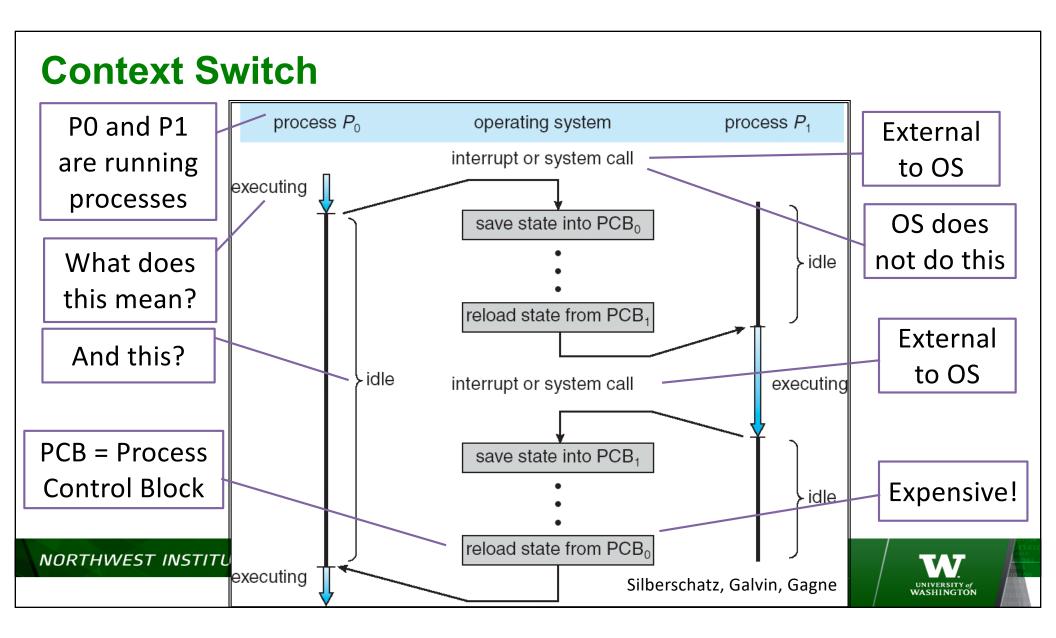
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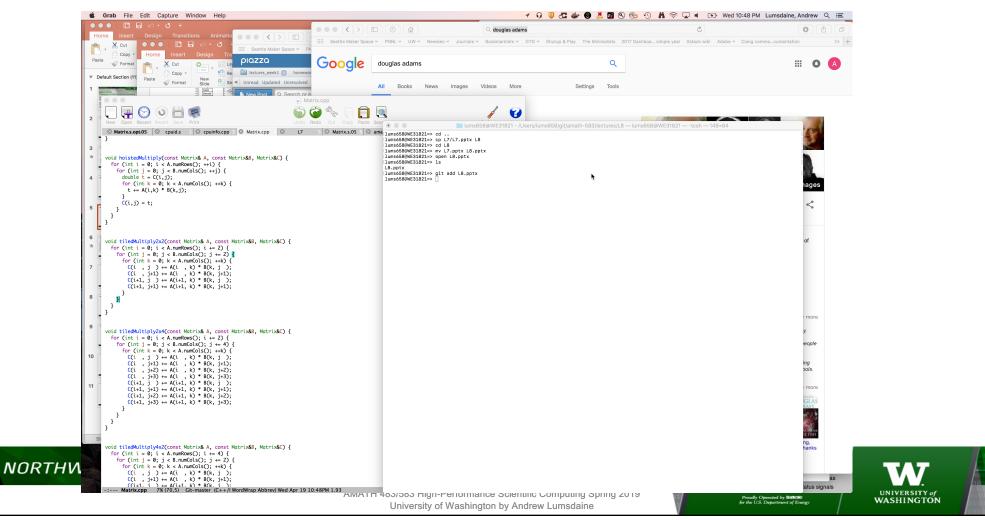


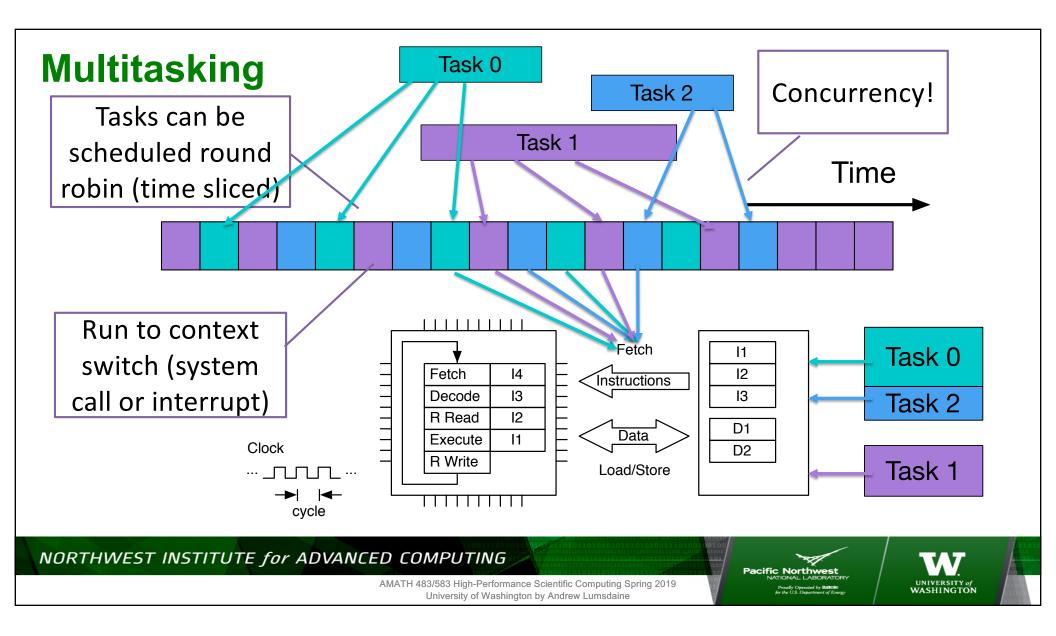
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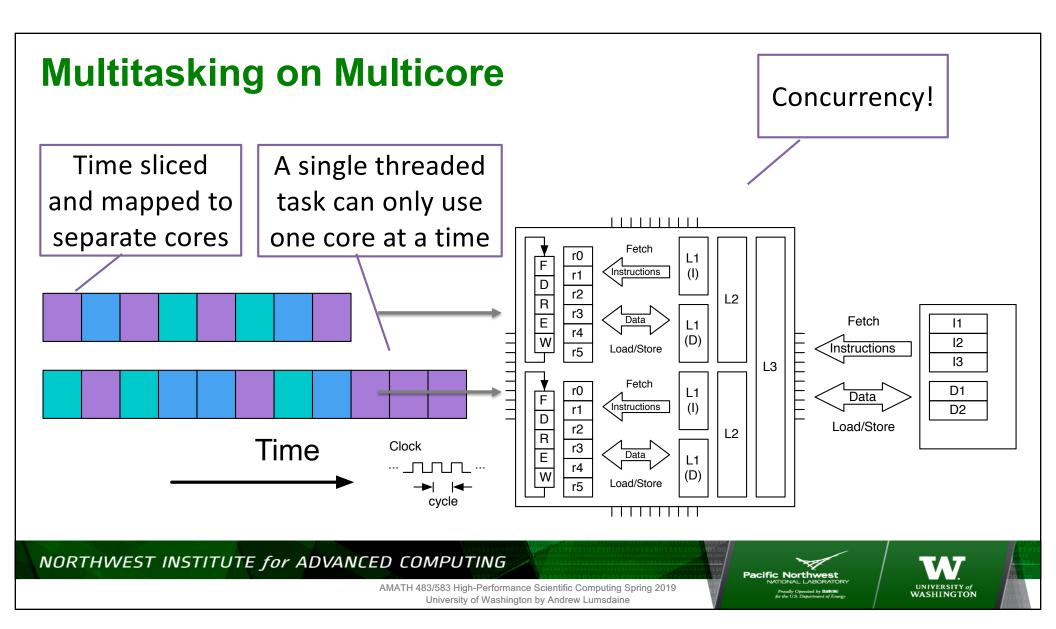


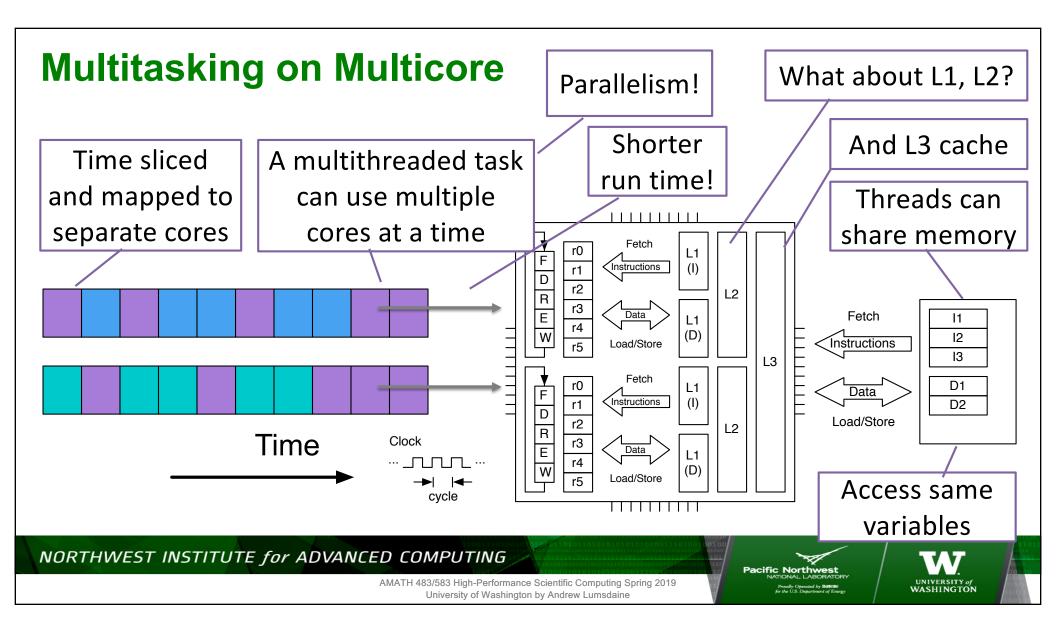


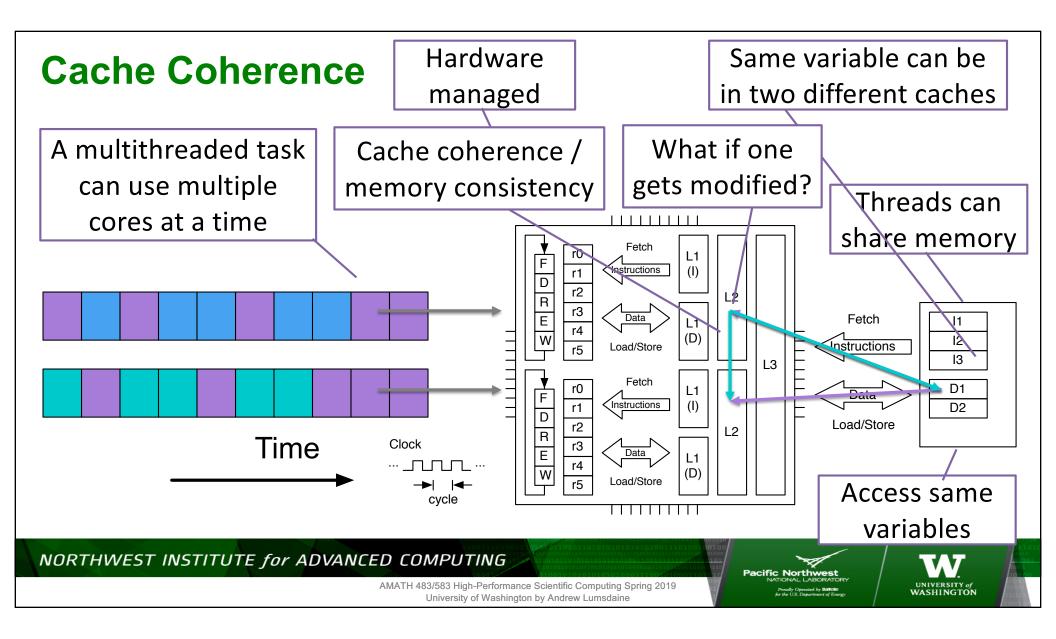
How Do We Run Multiple Programs Concurrently?

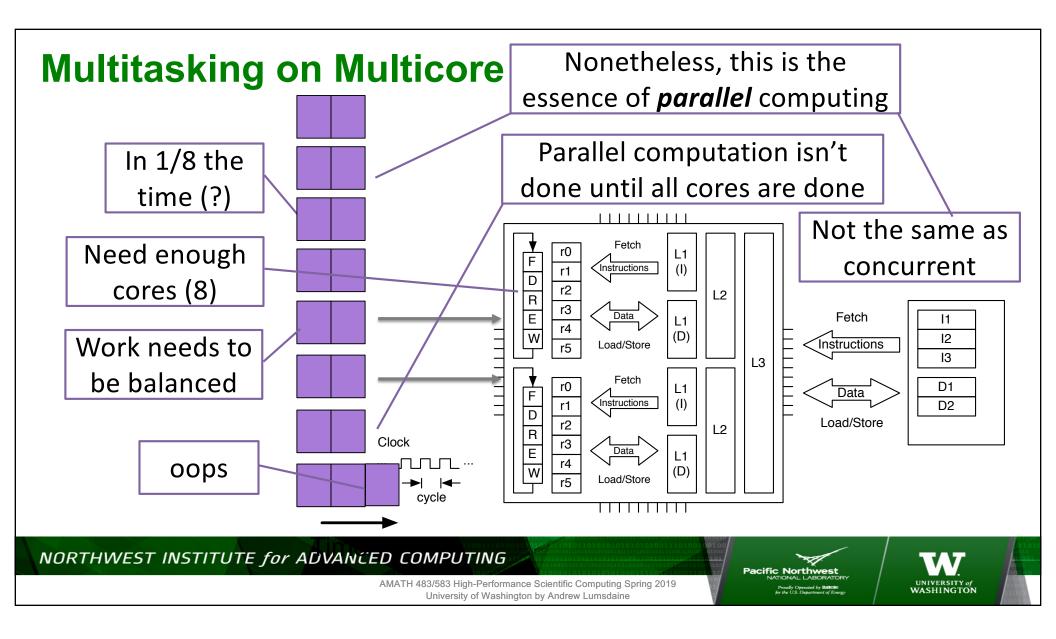


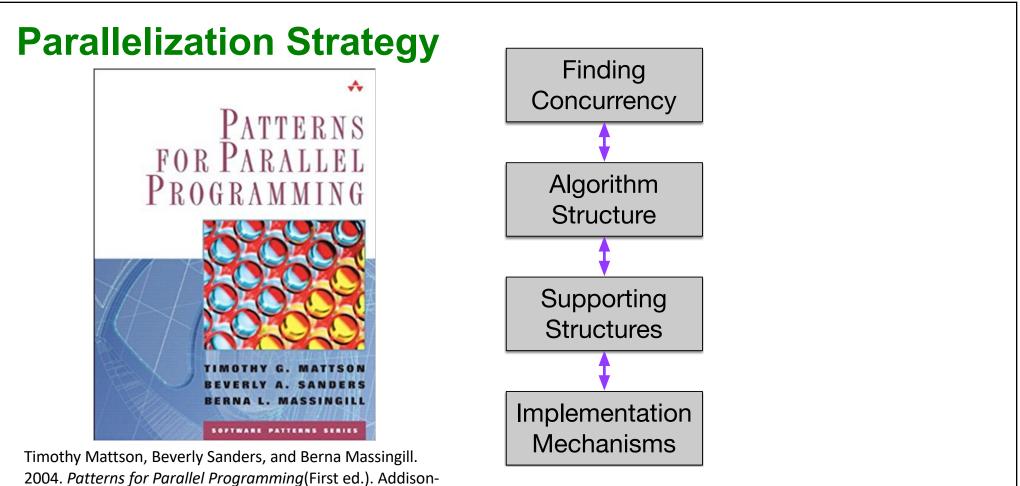






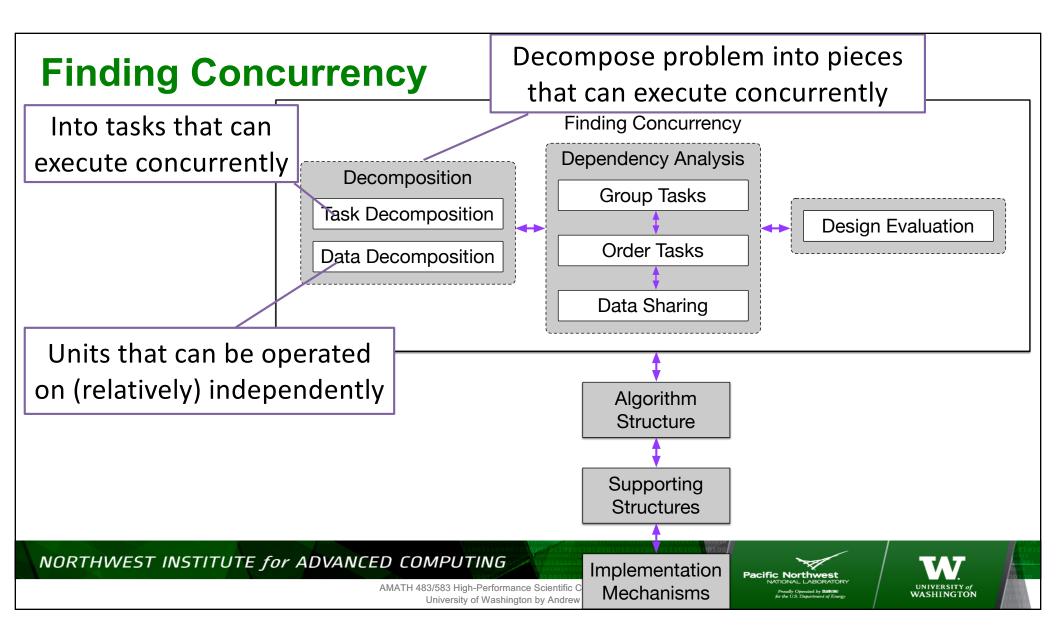


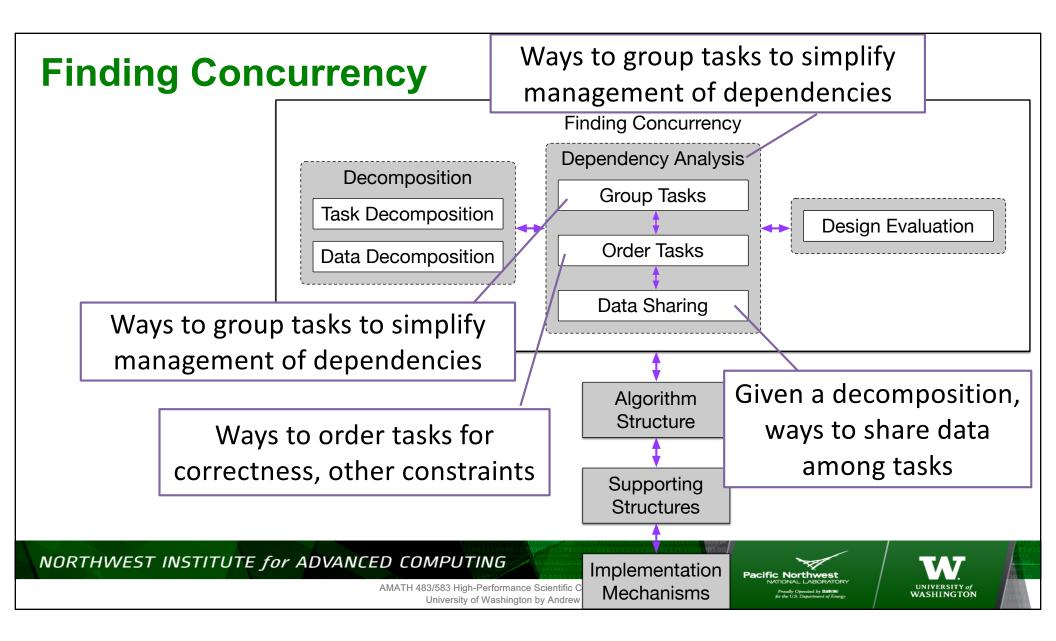


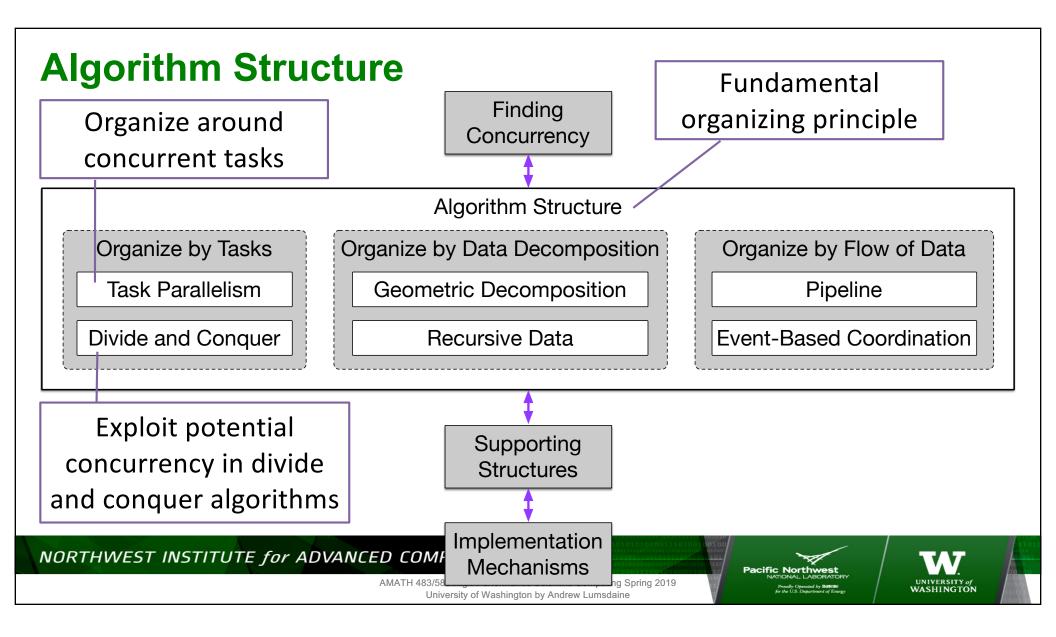


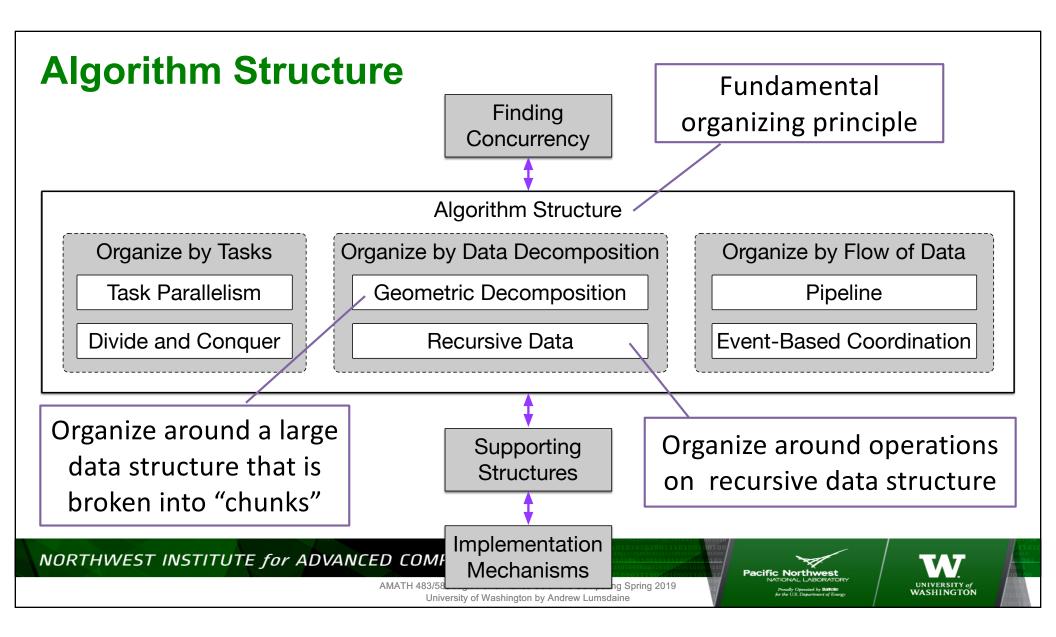
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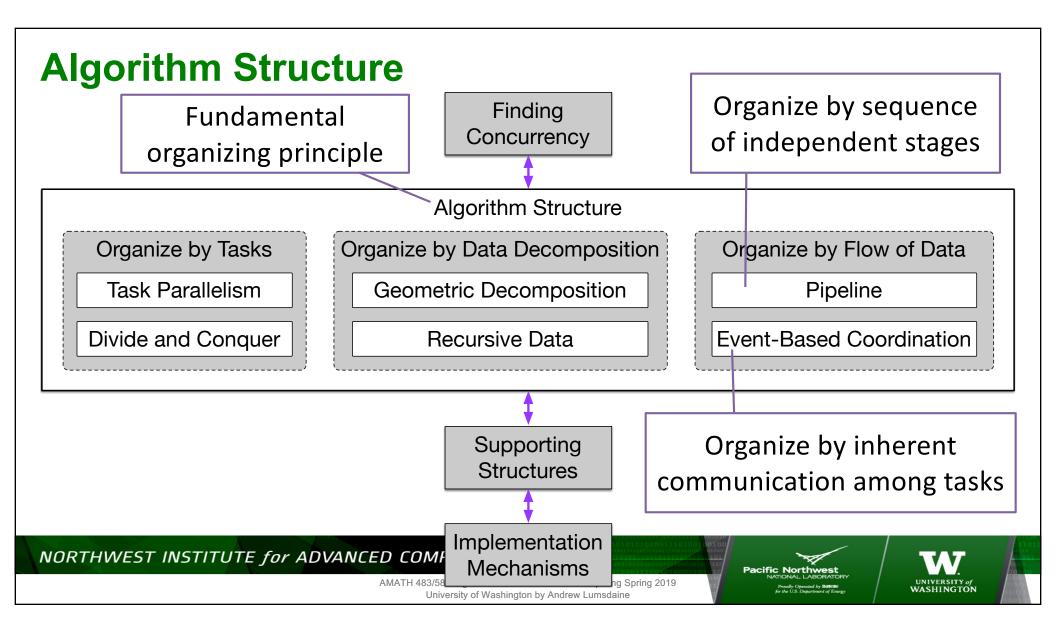


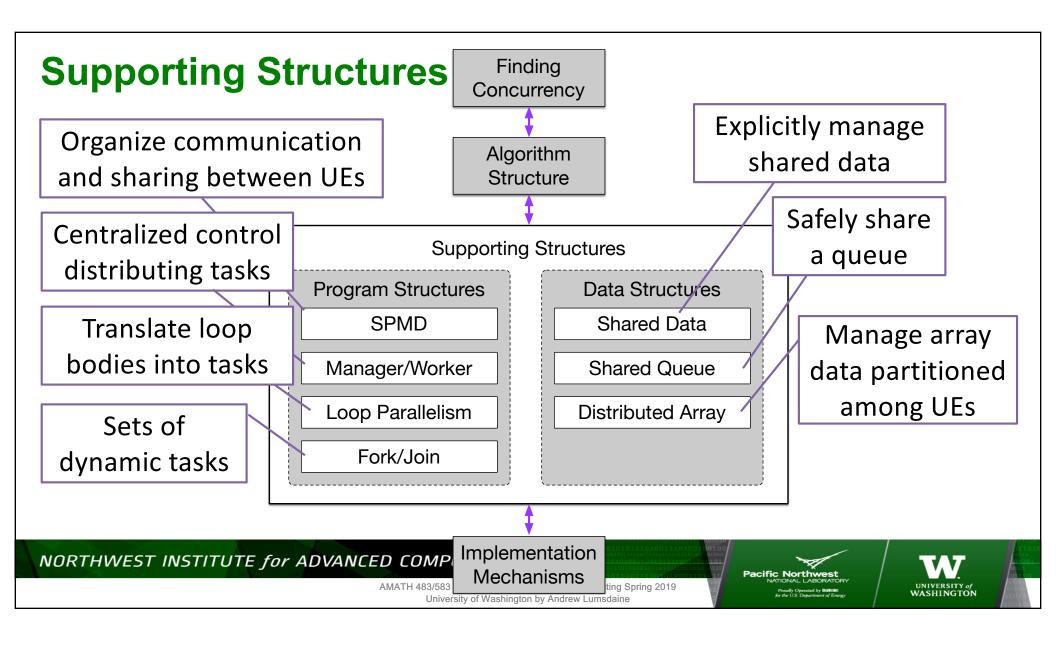


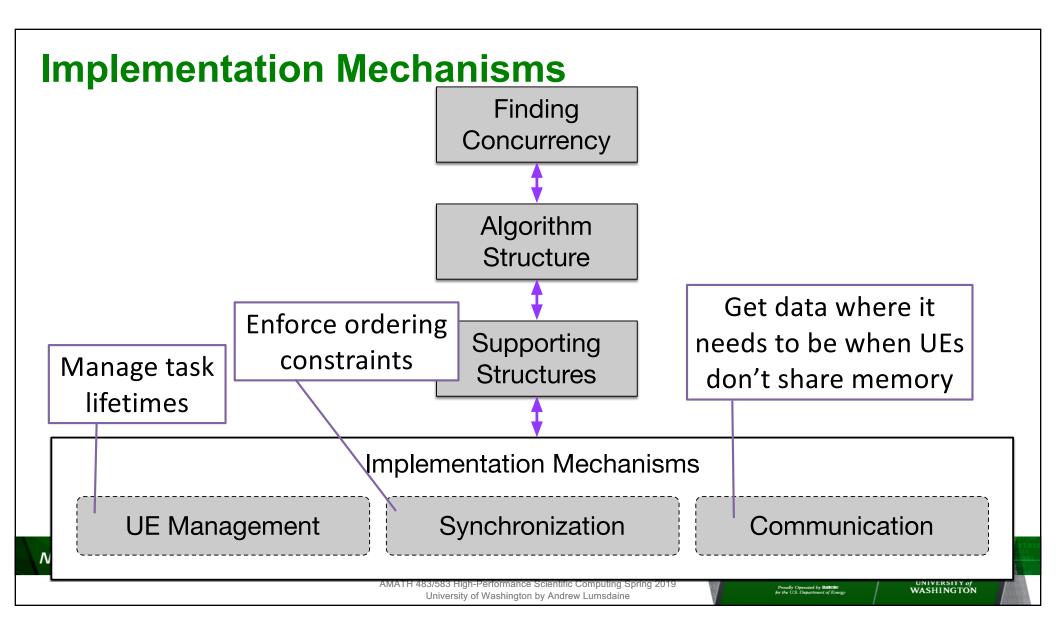






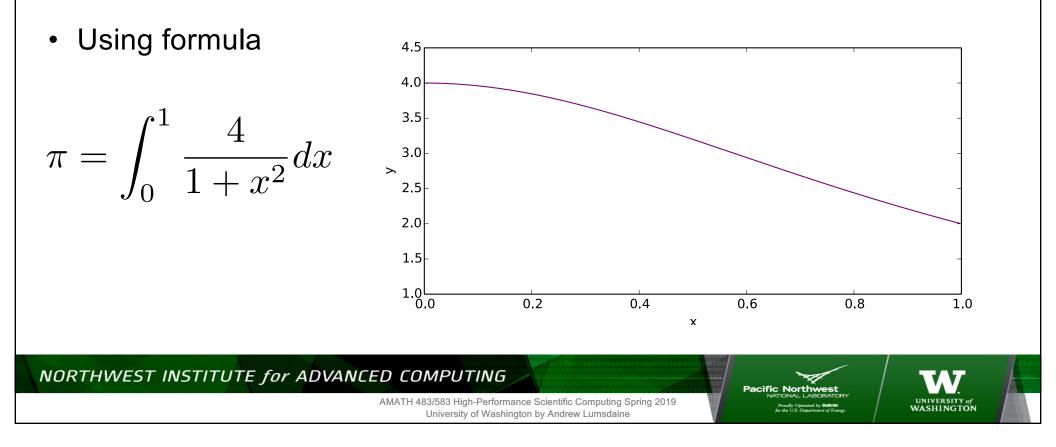


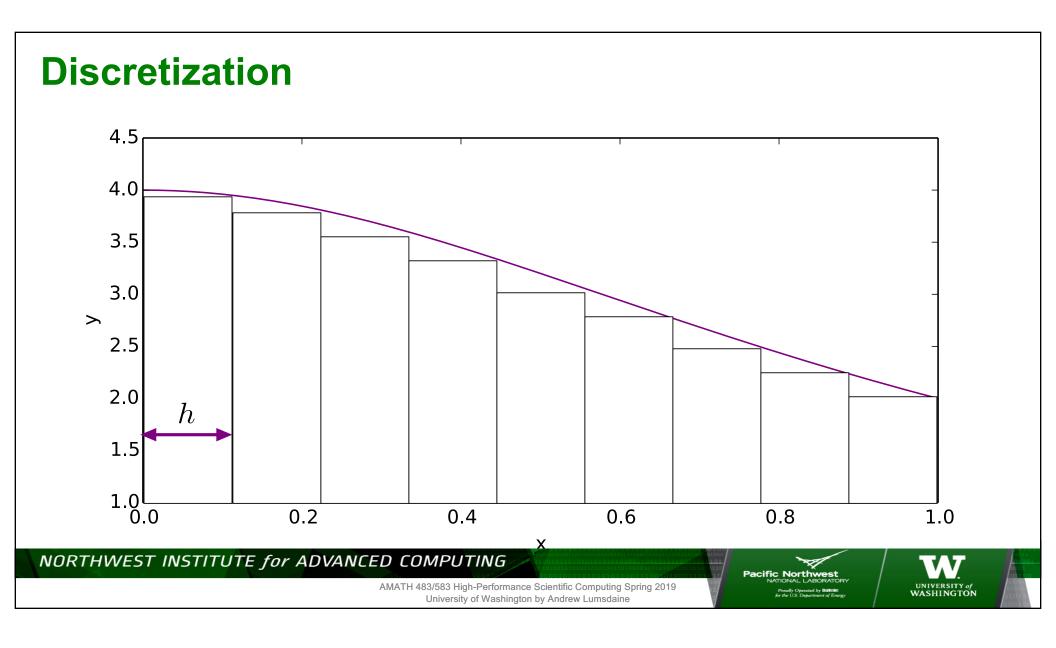




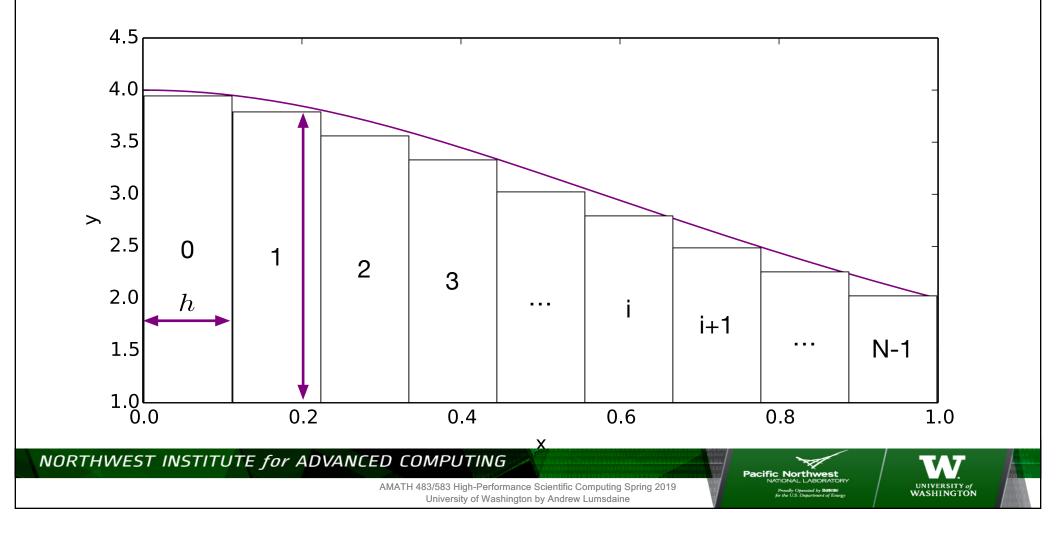
Example

- Find the value of π

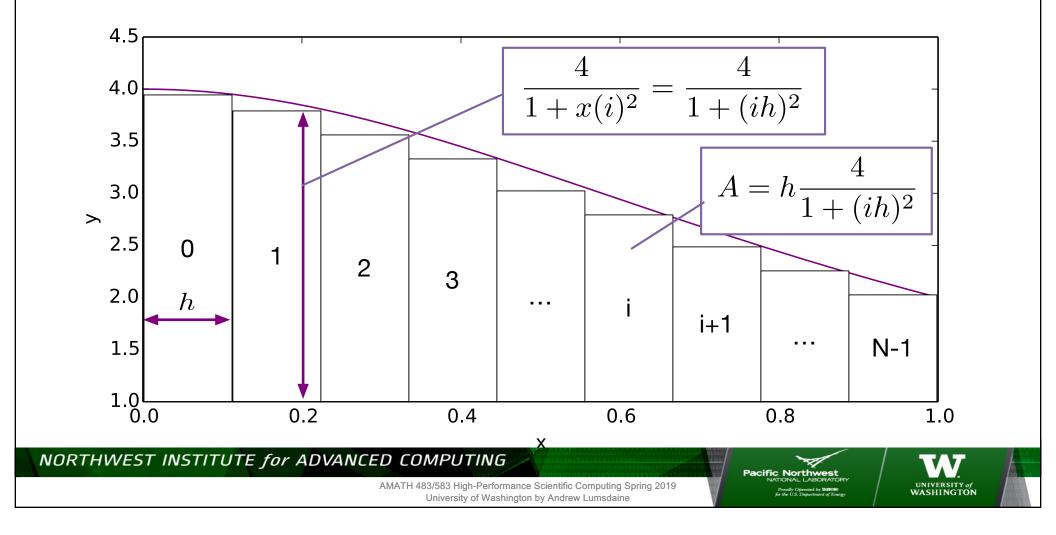




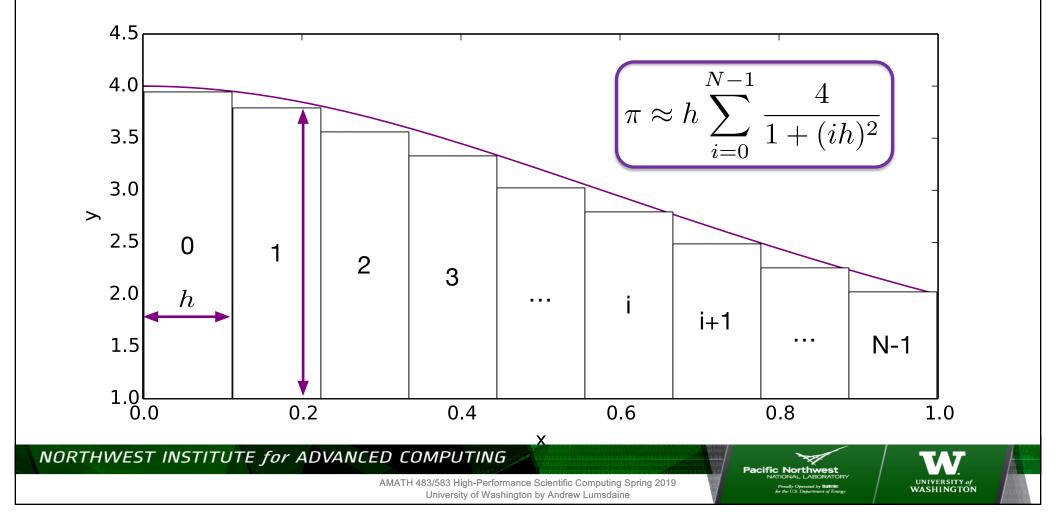




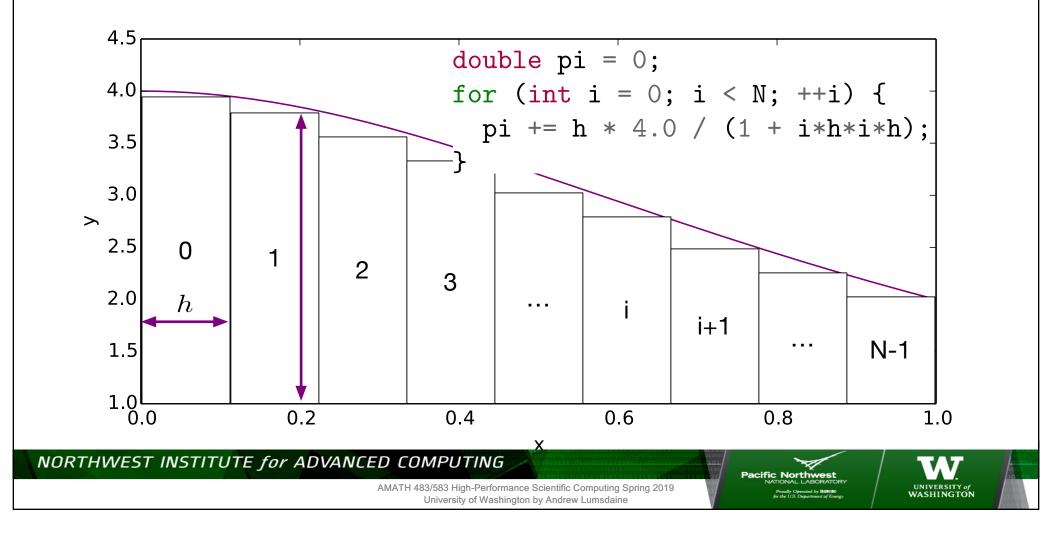


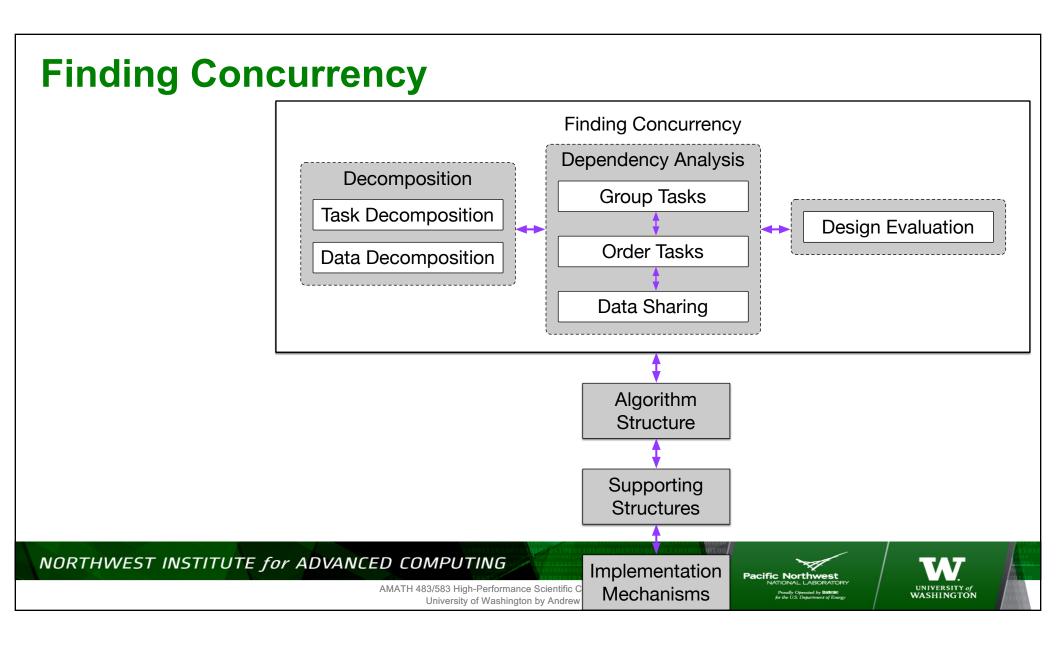


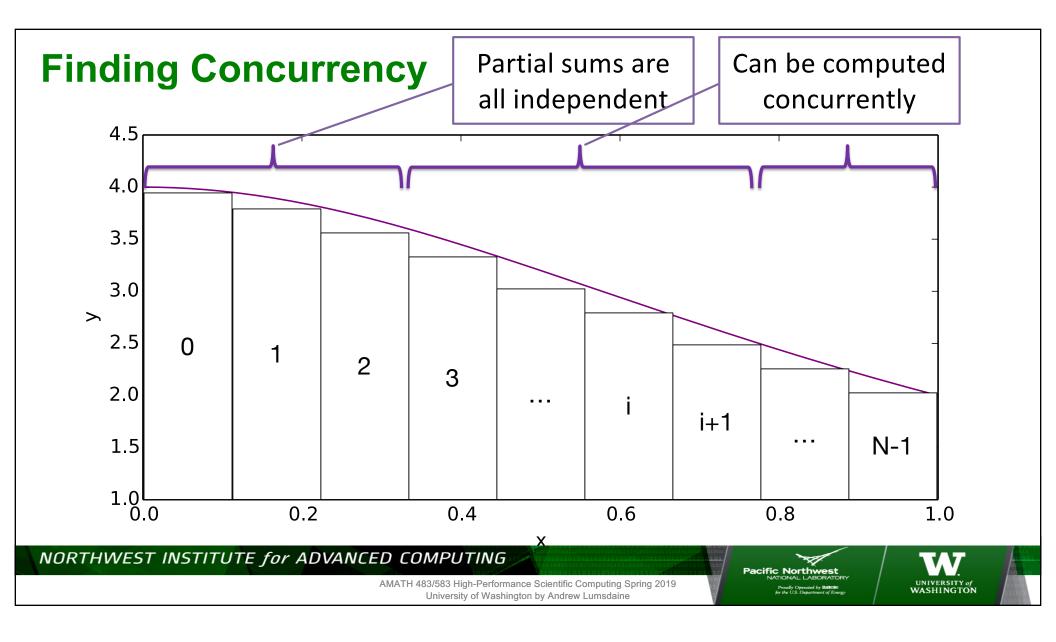


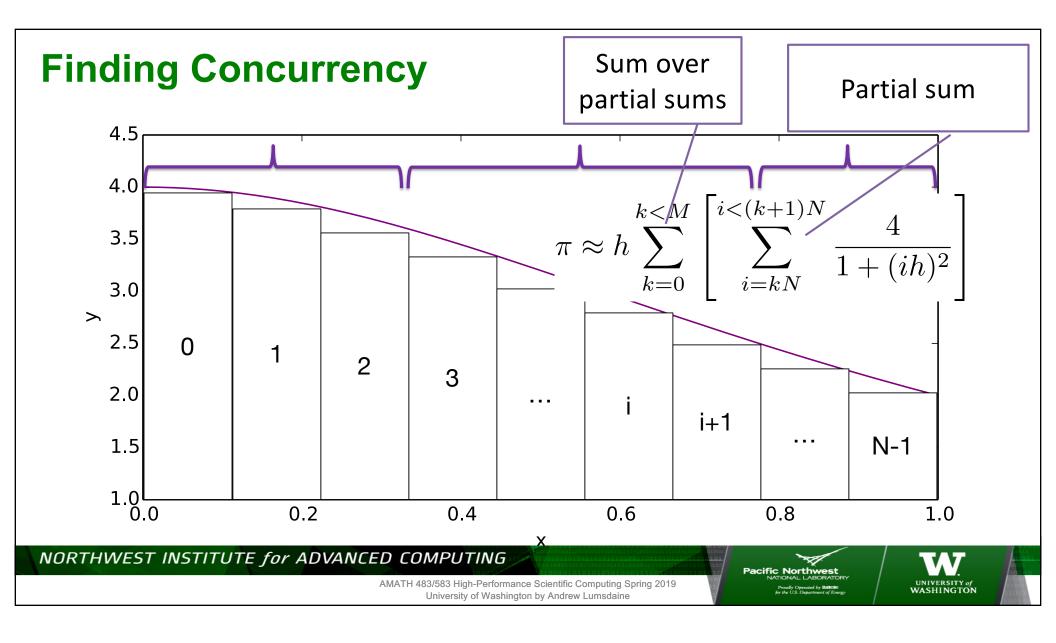


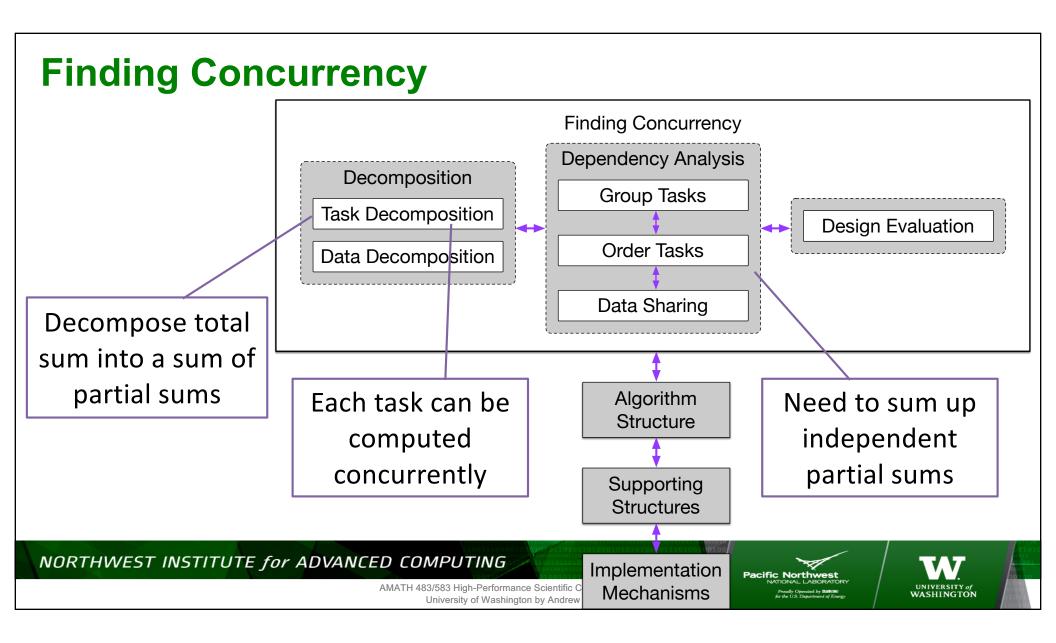


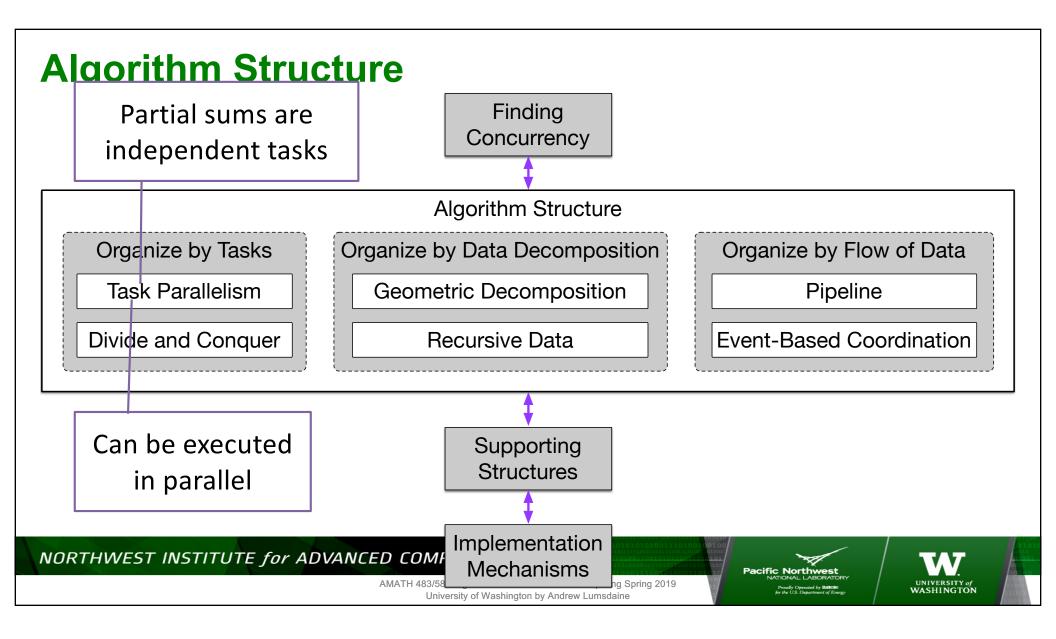


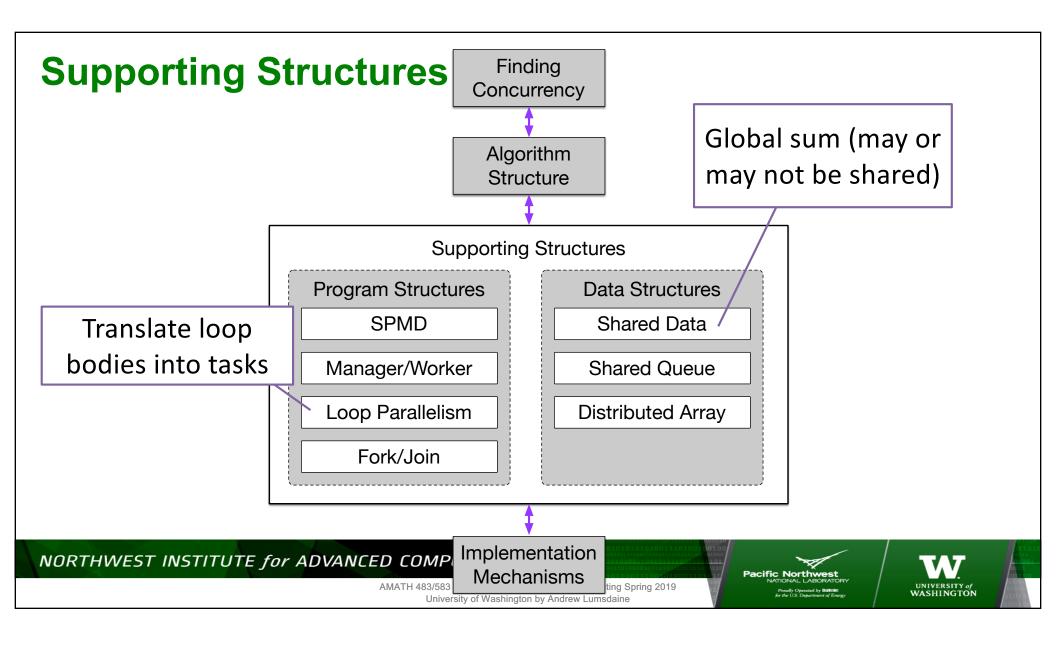


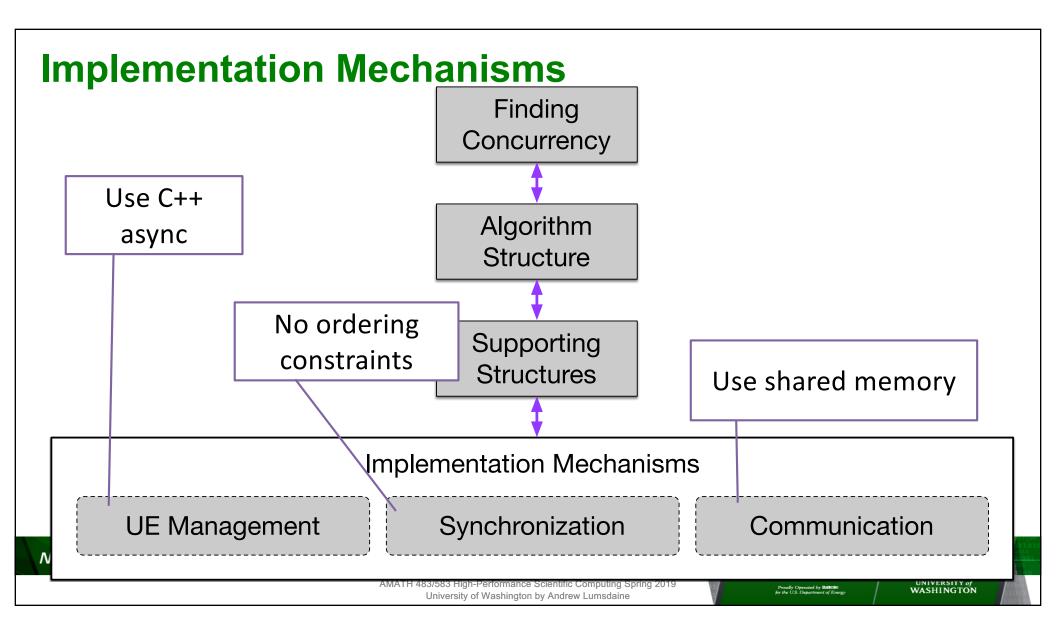


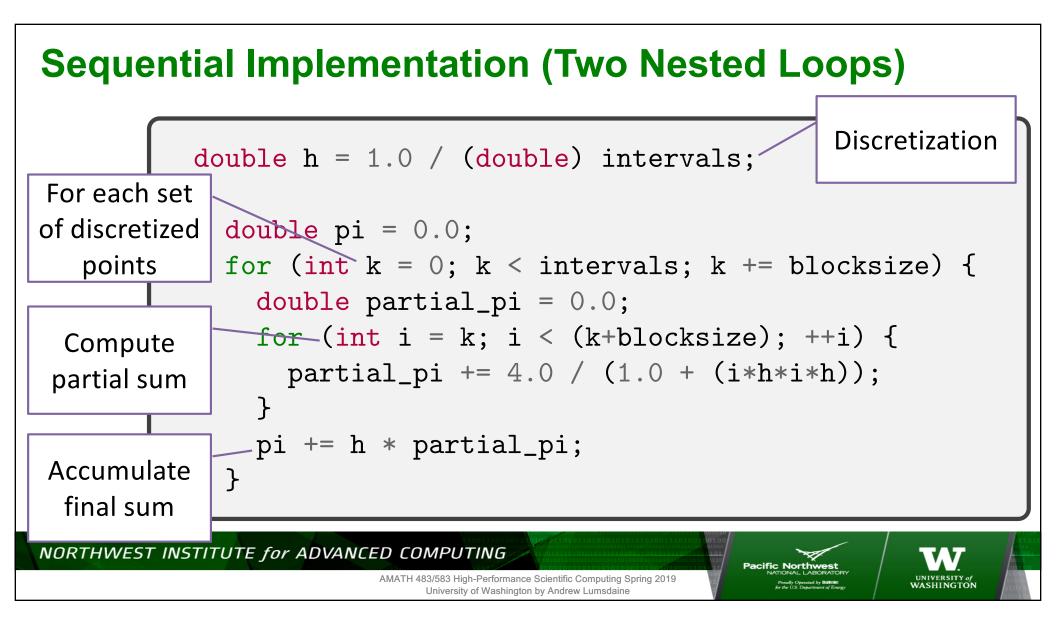




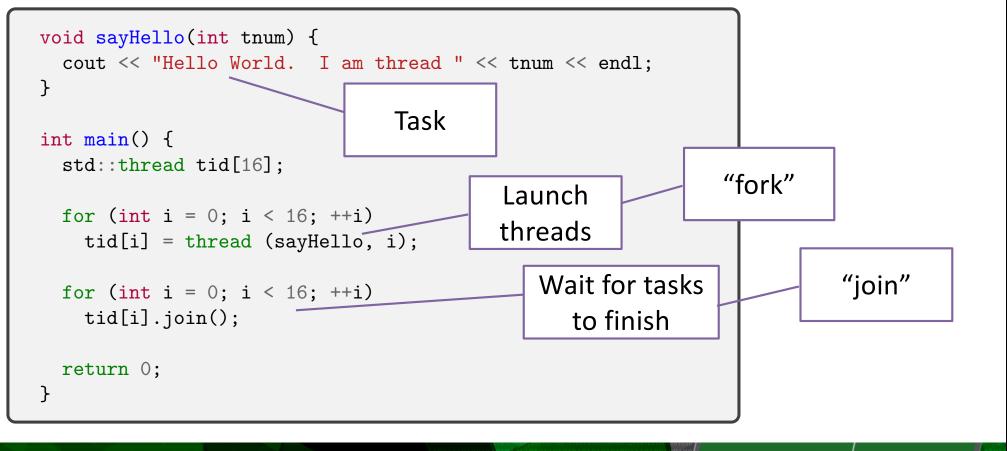








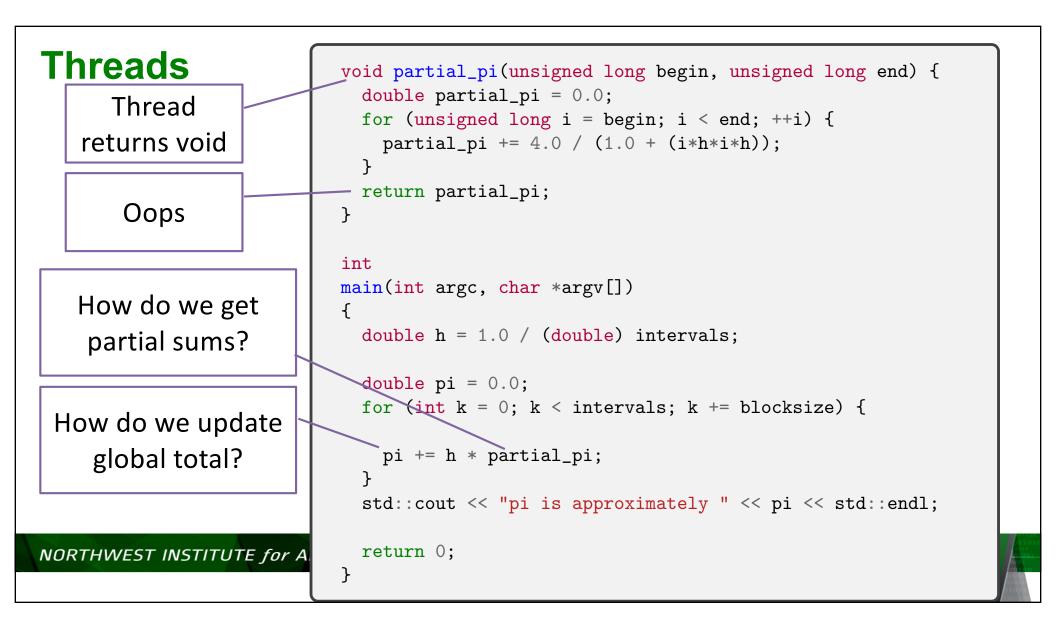


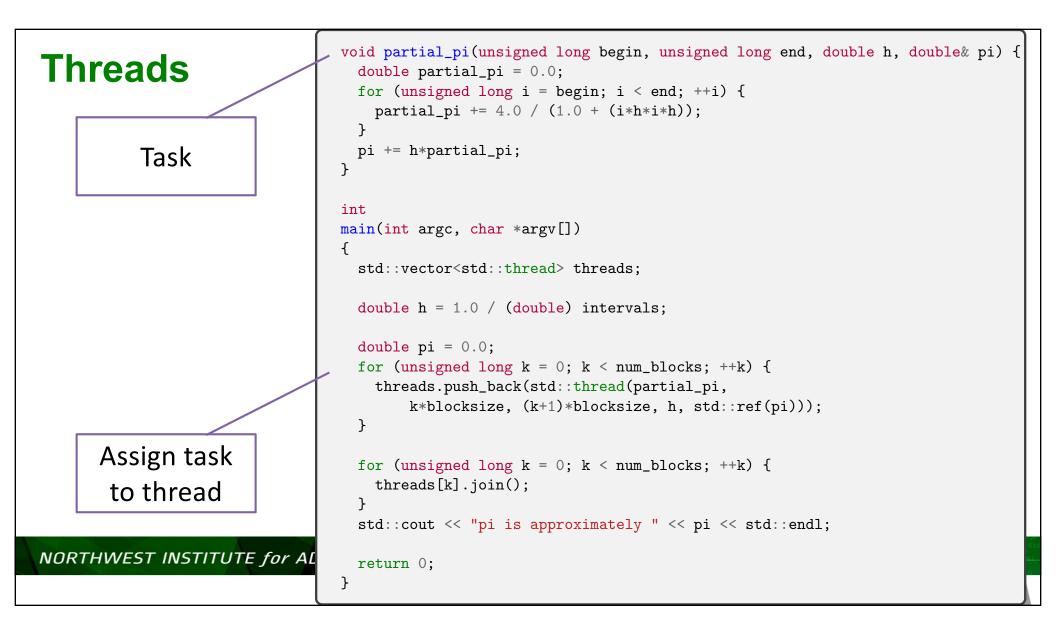


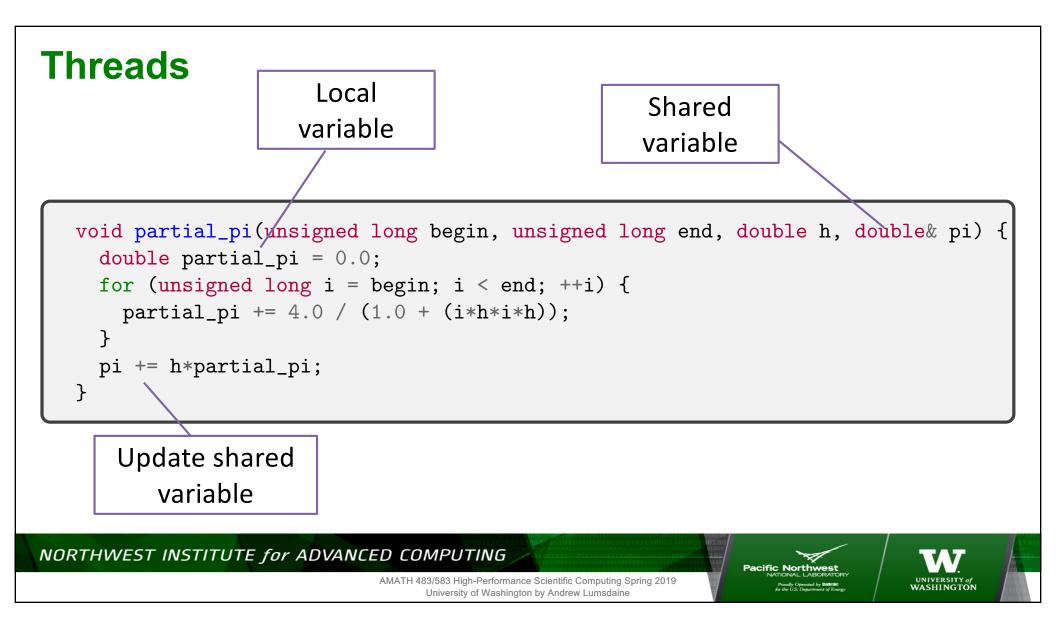
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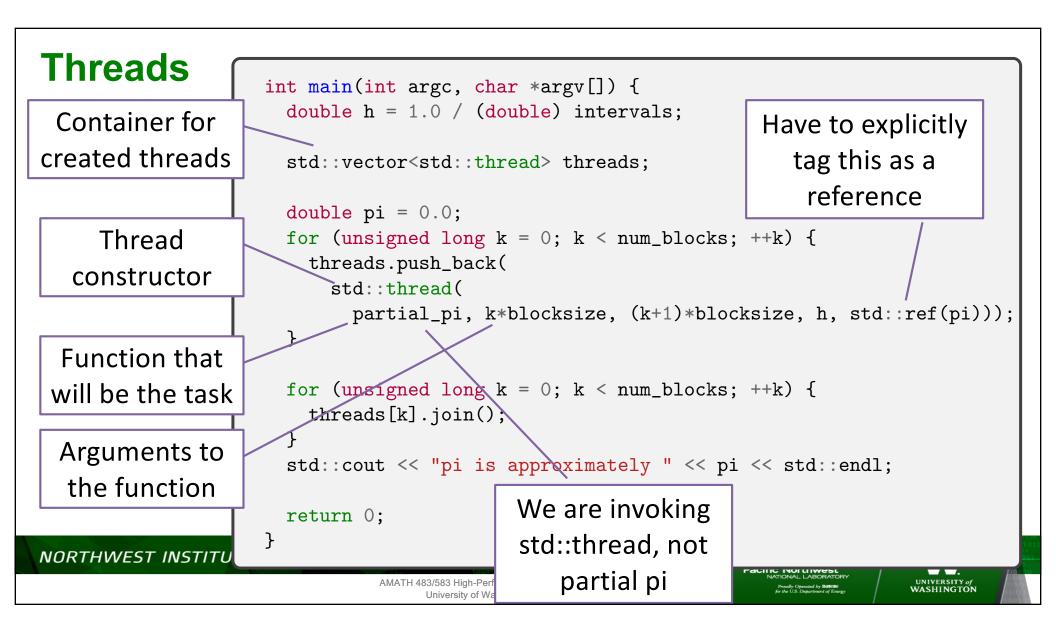
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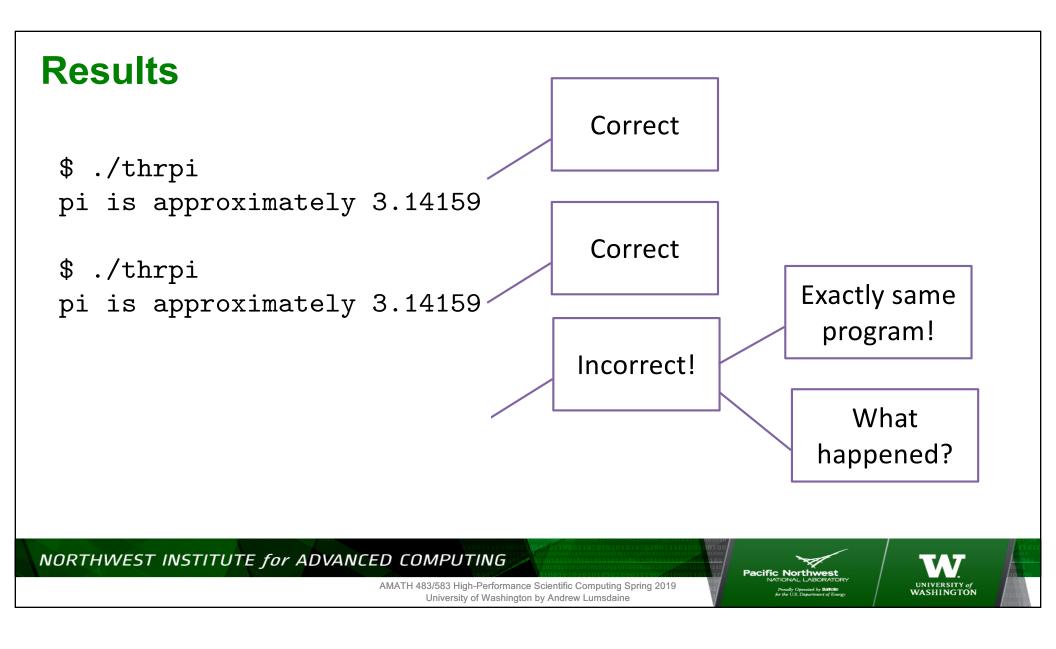


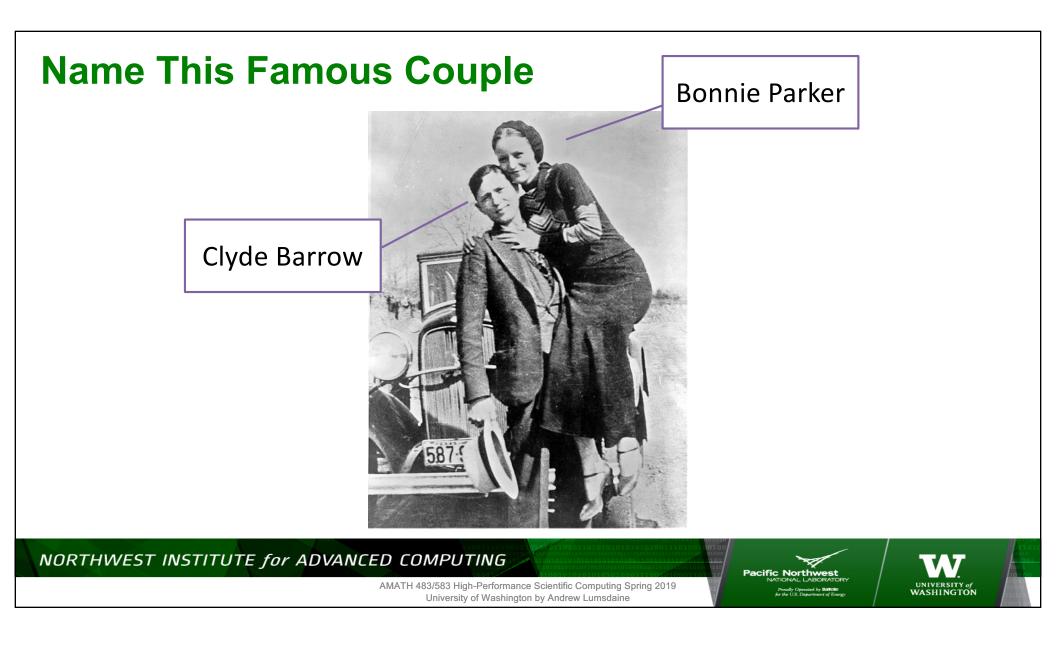












Bonnie and Clyde Use ATMs



```
int bank_balance = 300;
```

```
void withdraw(const string& msg, int amount) {
    int bal = bank_balance;
    string out_string = msg + " withdraws " + to_string(amount) + "\n";
    cout << out_string;
    bank_balance = bal - amount;
}</pre>
```

```
int main() {
   cout << "Starting balance is " << bank_balance << endl;</pre>
```

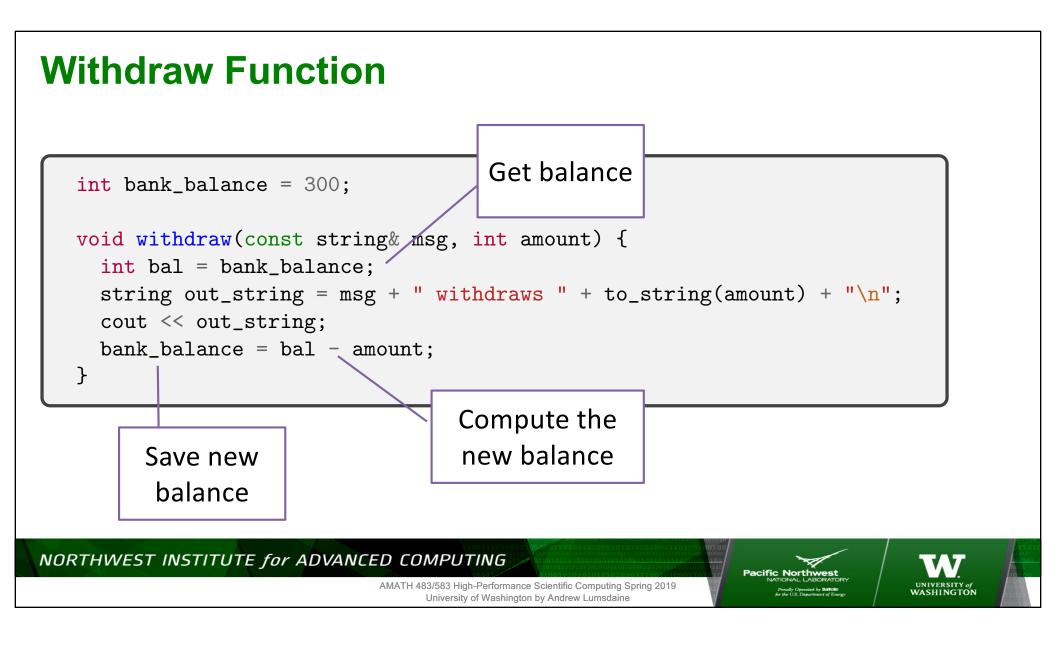
thread bonnie(withdraw, "Bonnie", 100); thread clyde(withdraw, "Clyde", 100);

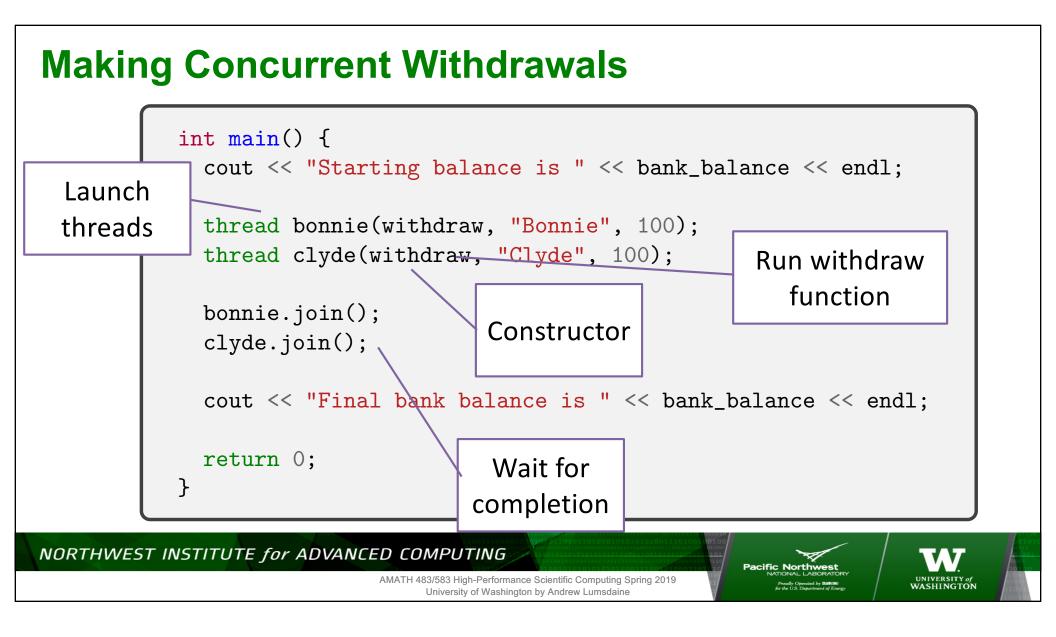
```
bonnie.join();
clyde.join();
```

cout << "Final bank balance is " << bank_balance << endl;</pre>

```
return 0;
```







Bonnie and Clyde Use ATMs



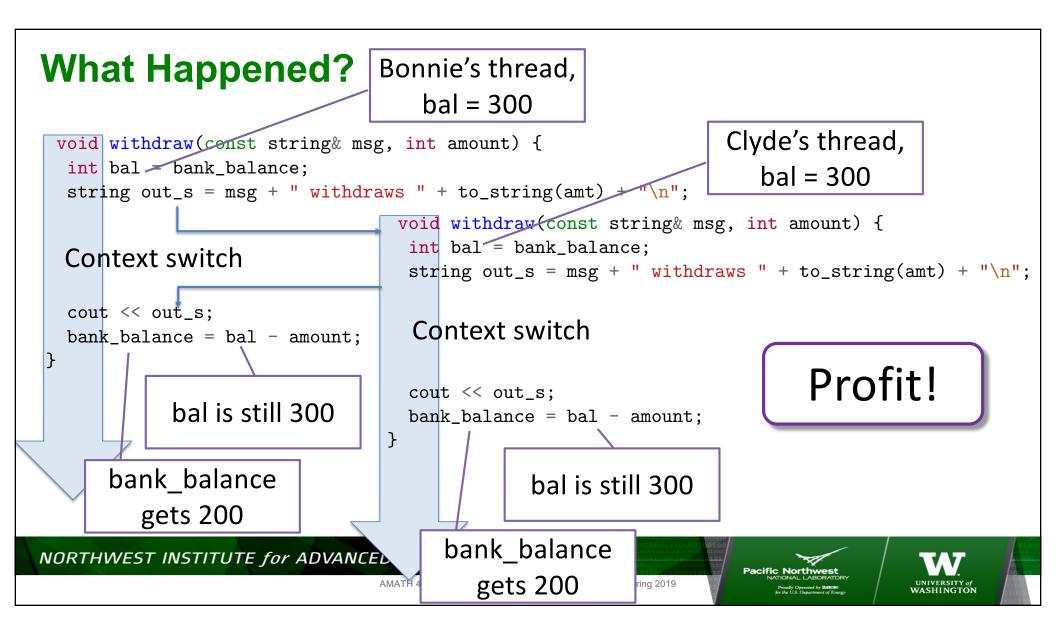
\$./a.out
Starting balance is 300
Bonnie withdraws 100
Clyde withdraws 100

Is this correct?

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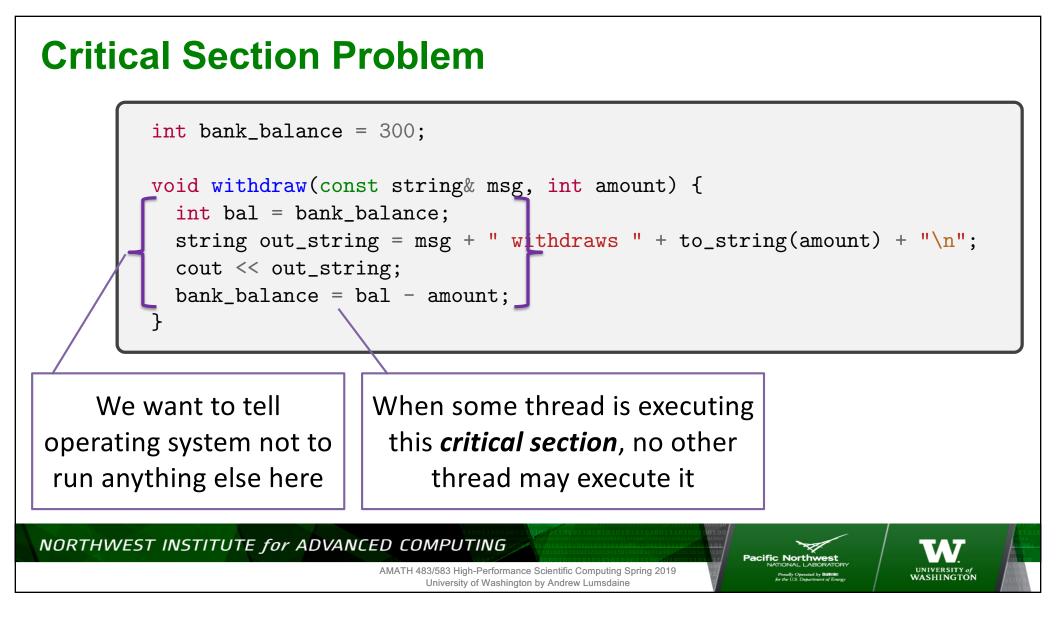
What Happened: Race Condition

- Final answer depends on instructions from different threads are interleaved with each other
- · Often occurs with shared writing of shared data
- Often due to read then update shared data
- What was true at the read is not true at the update

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The Critical-Section Problem

- n processes all competing to use some shared data
- Each process has a code segment, called critical section, in which the shared data is accessed.
- Problem ensure that when one process is executing in its critical section, no other process is allowed to execute in its critical section.
- What do we mean by "execute in its critical section"?

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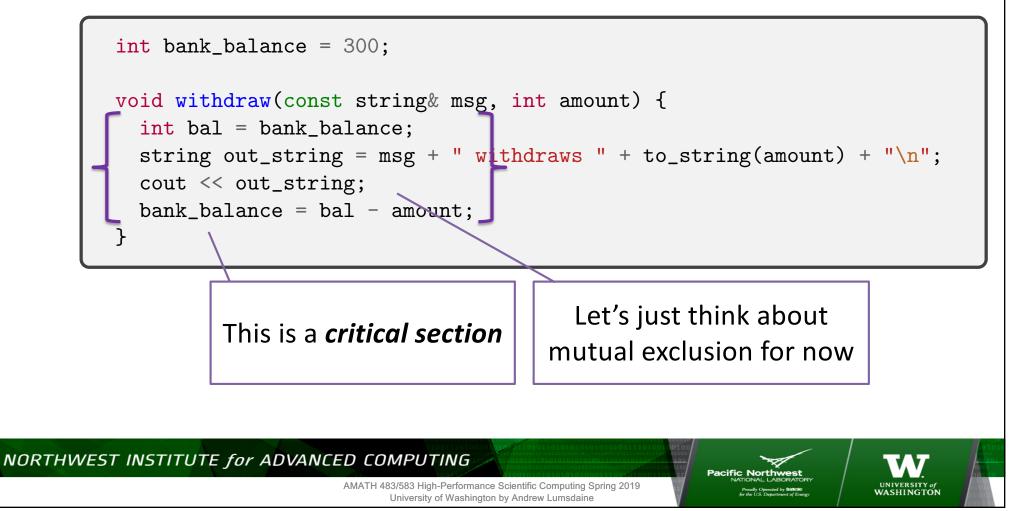


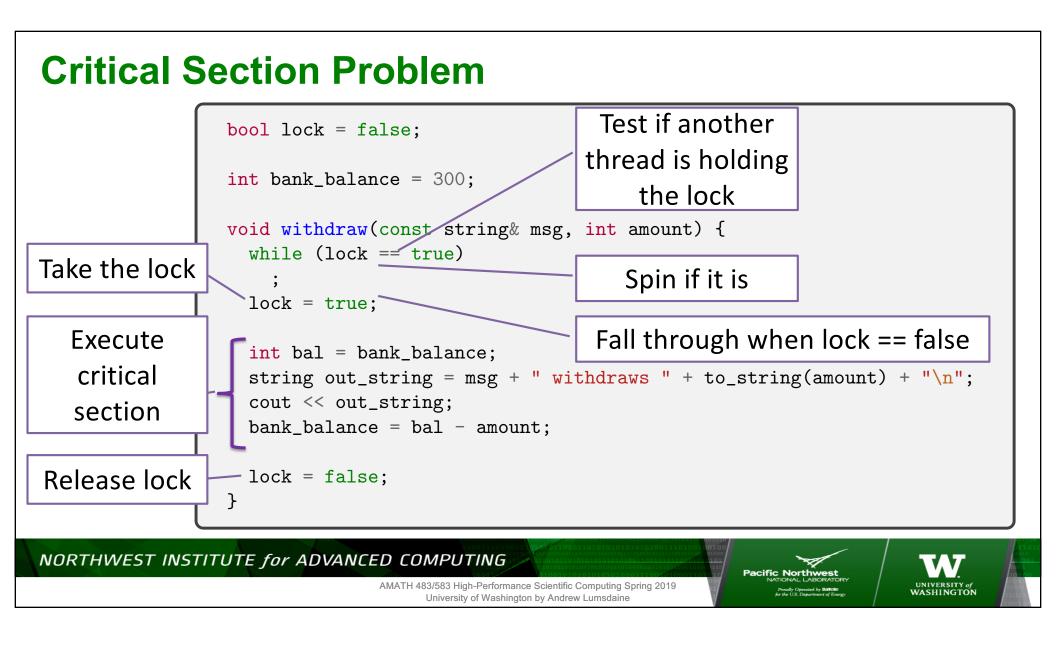
Solution to Critical-Section Problem

- Mutual Exclusion If process Pi is executing in its critical section, then no other processes can be executing in their critical sections
- Progress If no process is executing in its critical section and there
 exist some processes that wish to enter their critical section, then the
 selection of the processes that will enter the critical section next
 cannot be postponed indefinitely
- Bounded Waiting A bound must exist on the number of times that other processes are allowed to enter their critical sections after a process has made a request to enter its critical section and before that request is granted
 - Assume that each process executes at a nonzero speed
 - No assumption concerning relative speed of the N processes

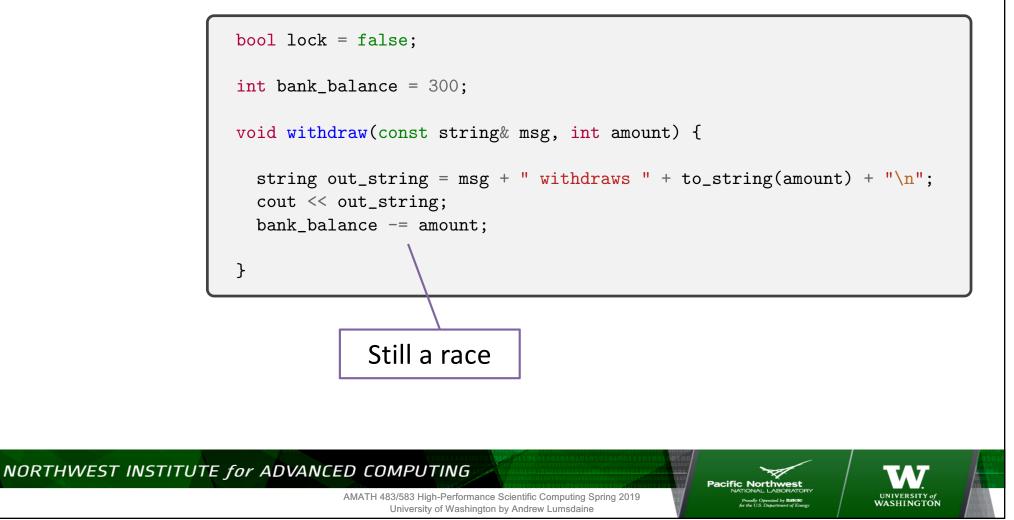


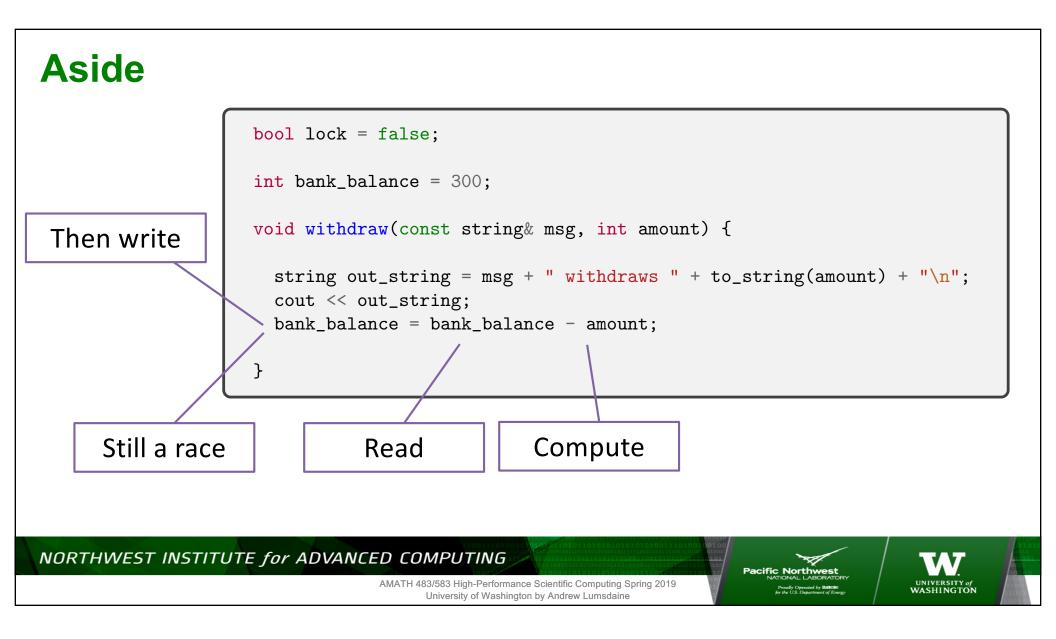


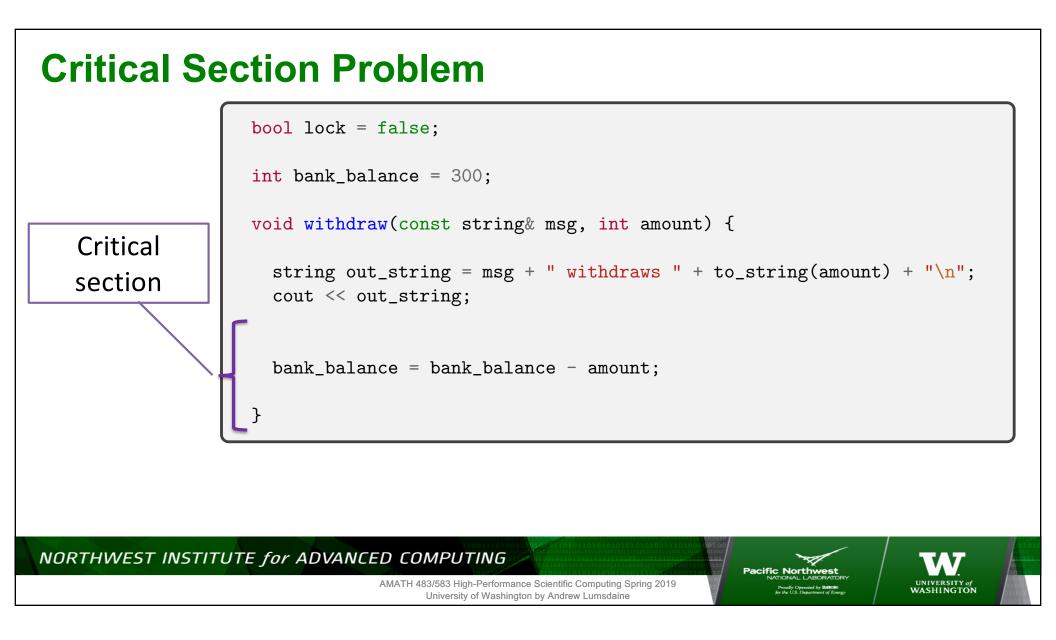


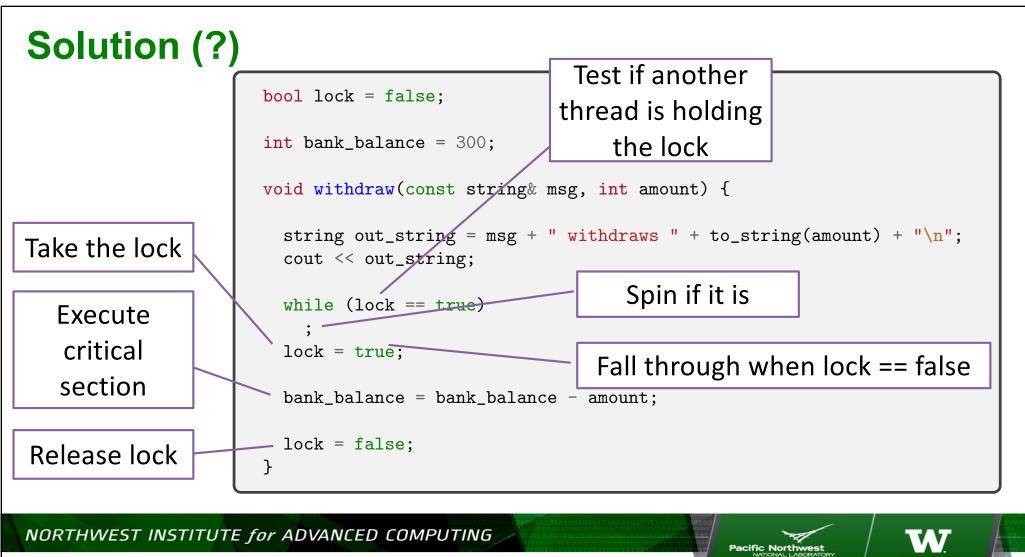


Aside

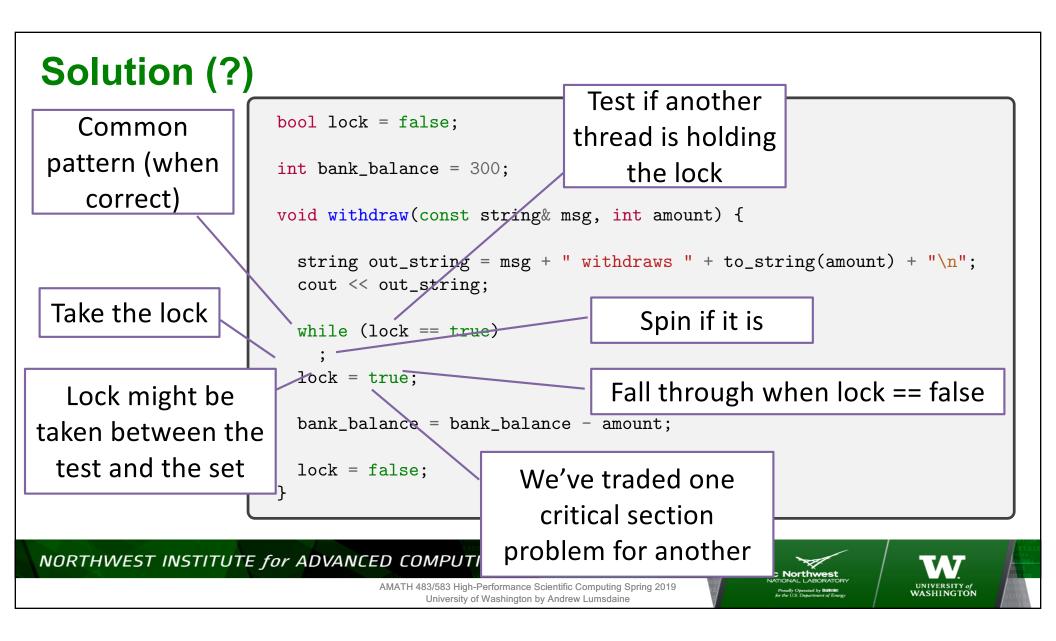








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Synchronization Hardware

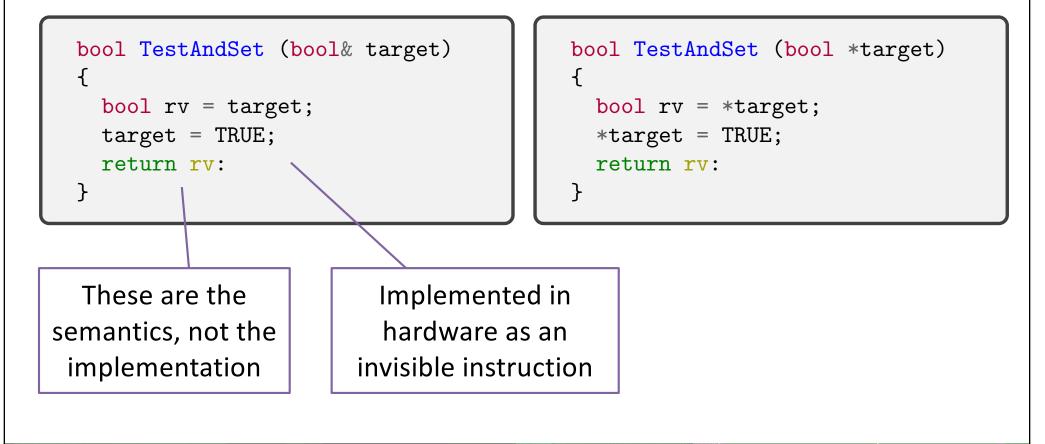
- Many systems provide hardware support for critical section code
- Uniprocessors could disable interrupts
 - Currently running code would execute without preemption
 - Generally too inefficient on multiprocessor systems
 - Operating systems using this not broadly scalable
- Modern machines provide special *atomic* hardware instructions
 - Atomic = non-interruptable
 - Either test memory word and set value
 - Or swap contents of two memory words

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Test and Set

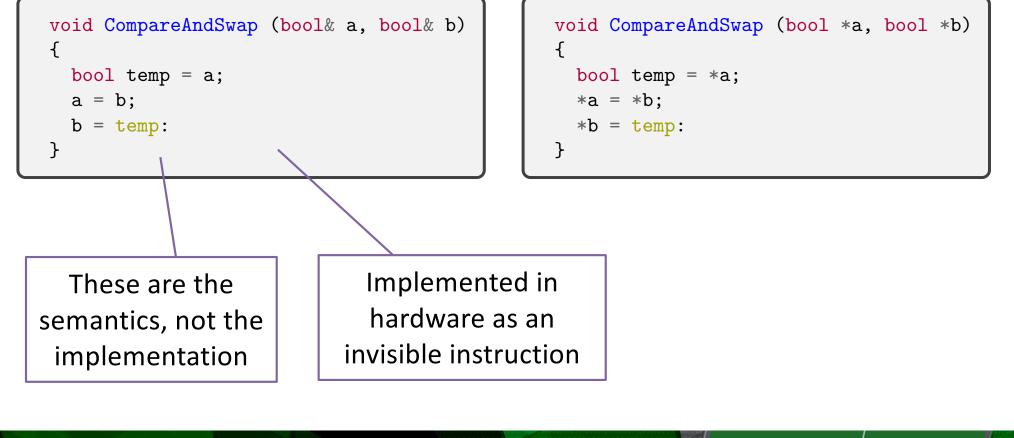


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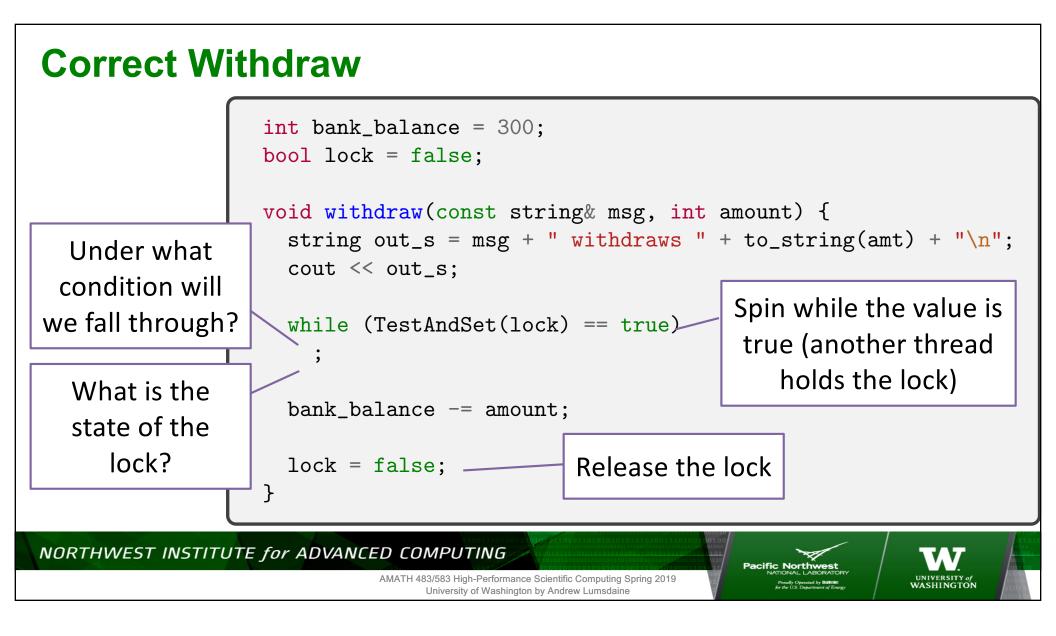


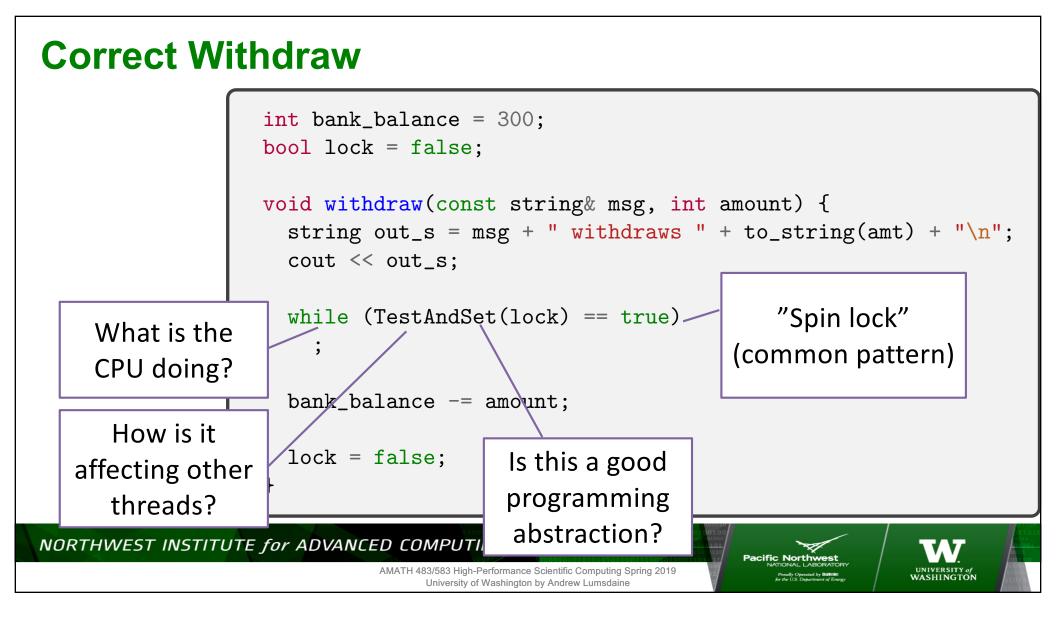
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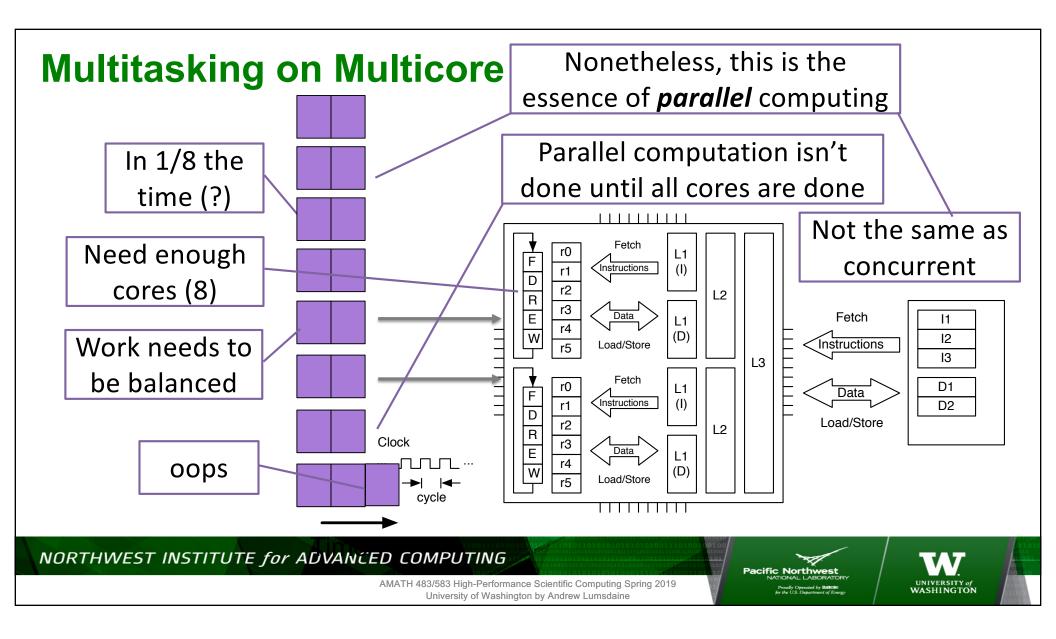
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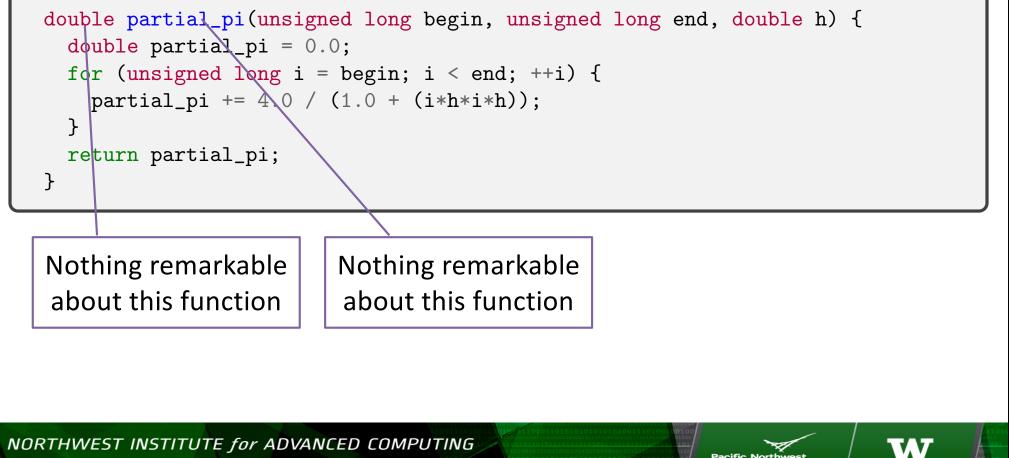
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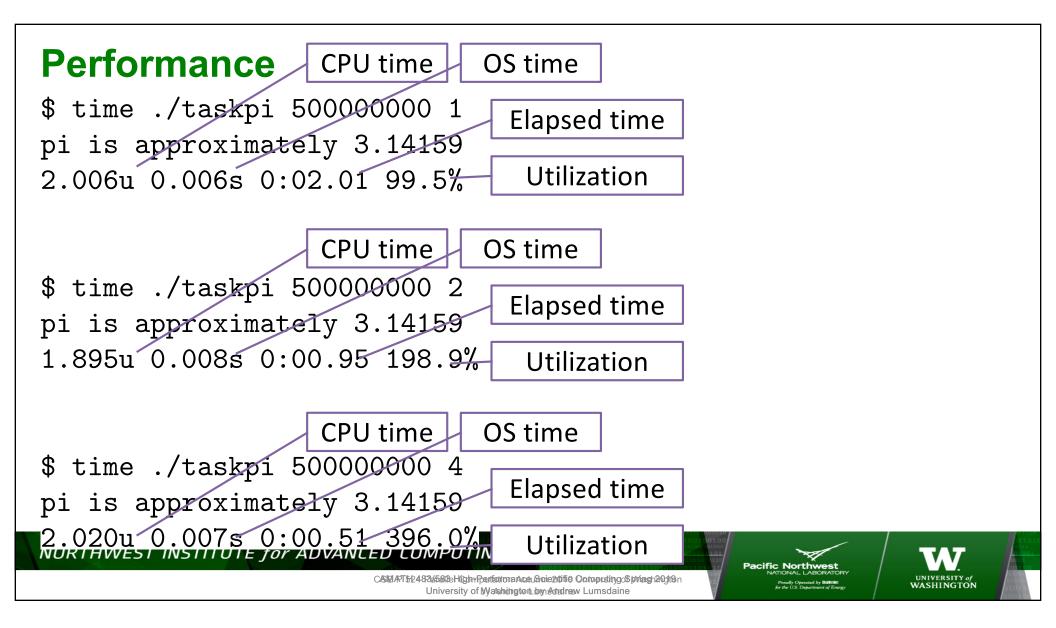


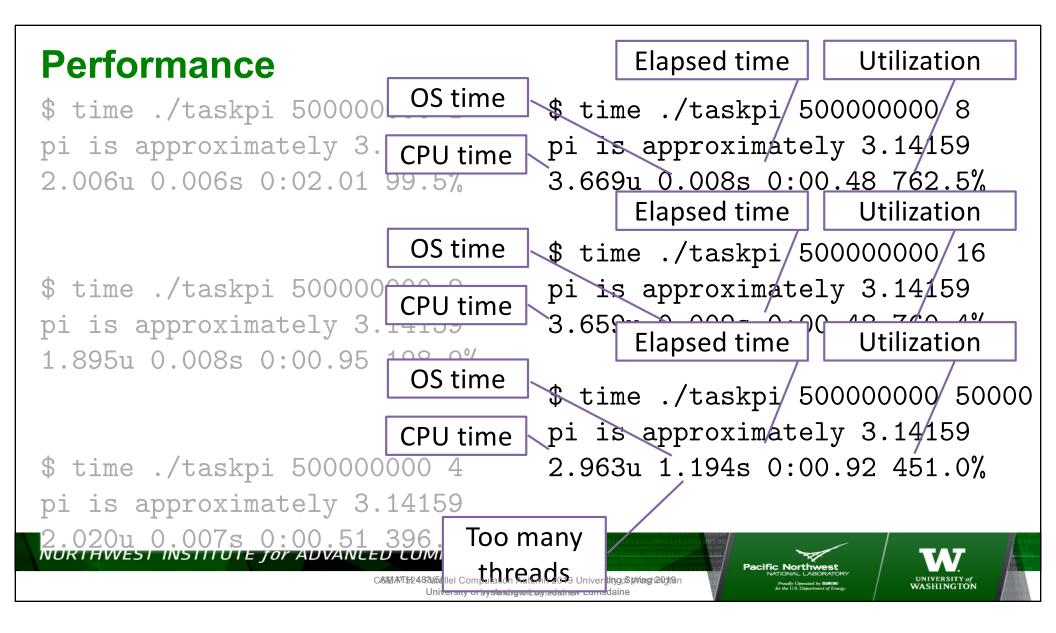


Numerical Quadrature Task

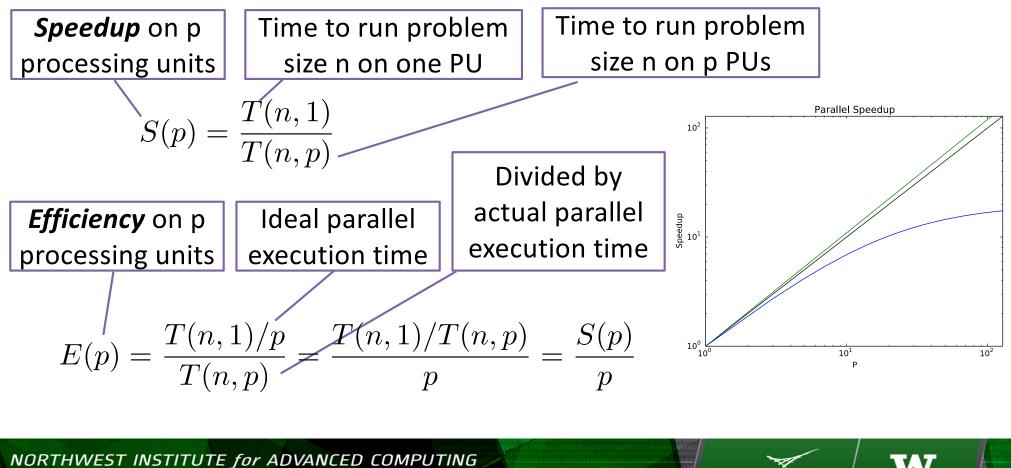


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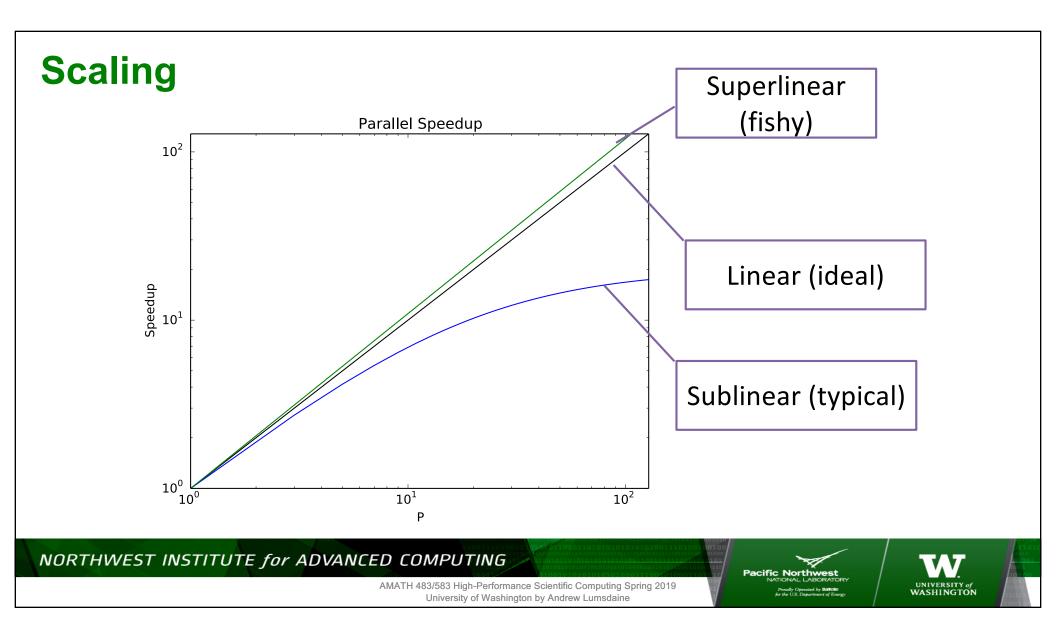




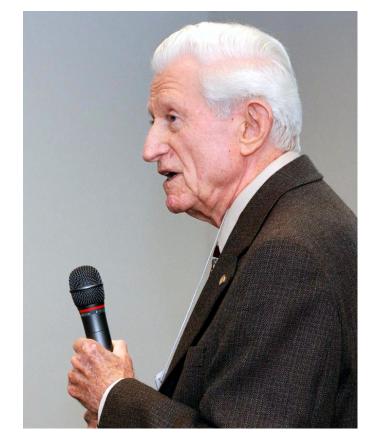




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Name This Famous Person



"Validity of the single processor approach to achieving large-scale computing capabilities," AFIPS Conference Proceedings (30): 483–485, 1967.

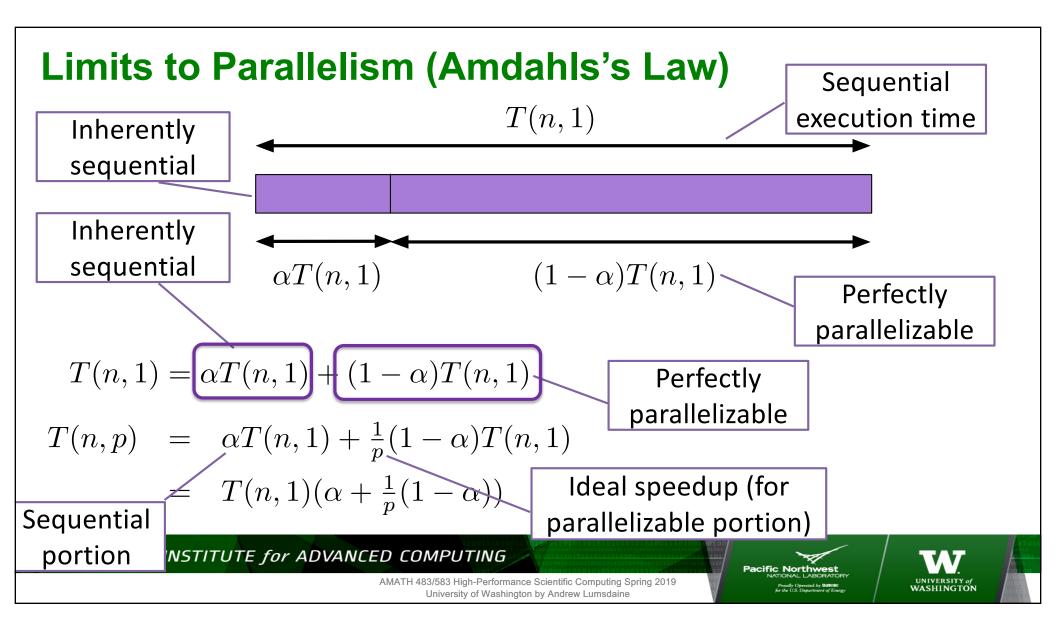
Gene Amdahl (1922-2015)

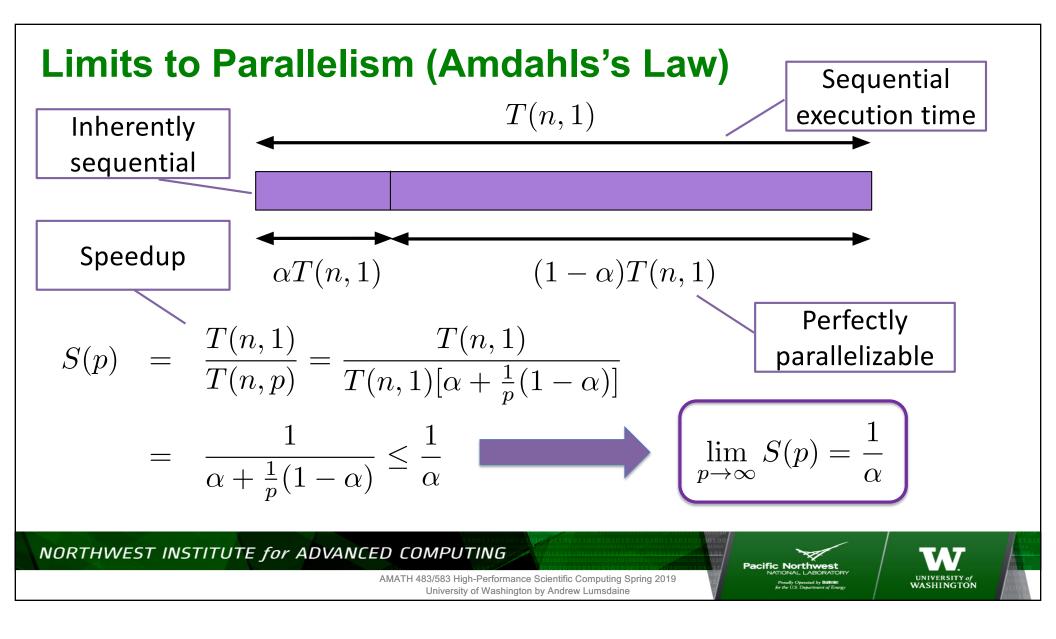
Amdahl's Law

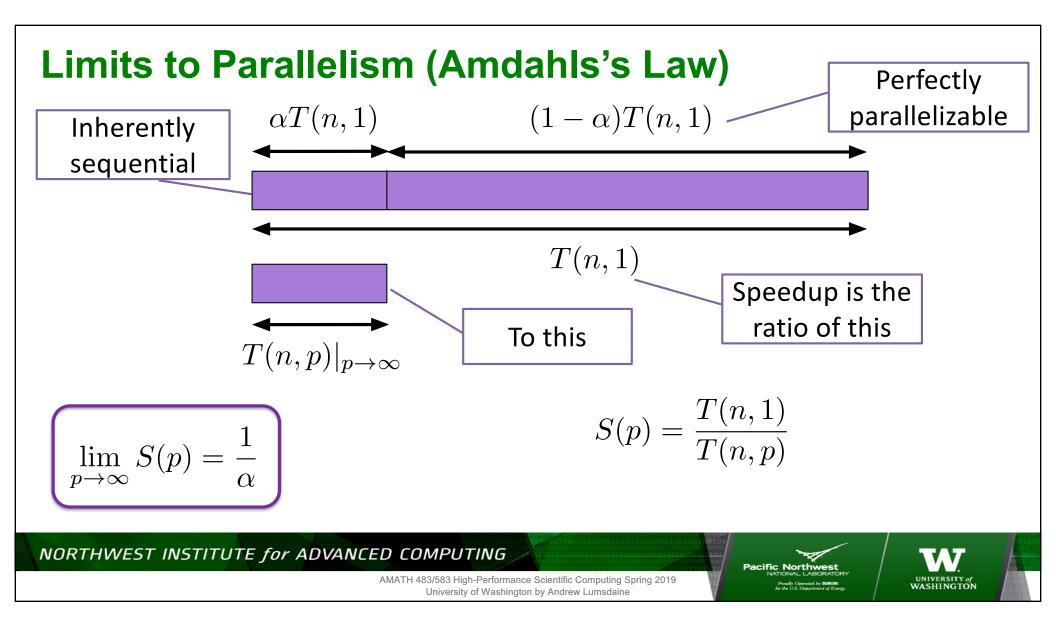
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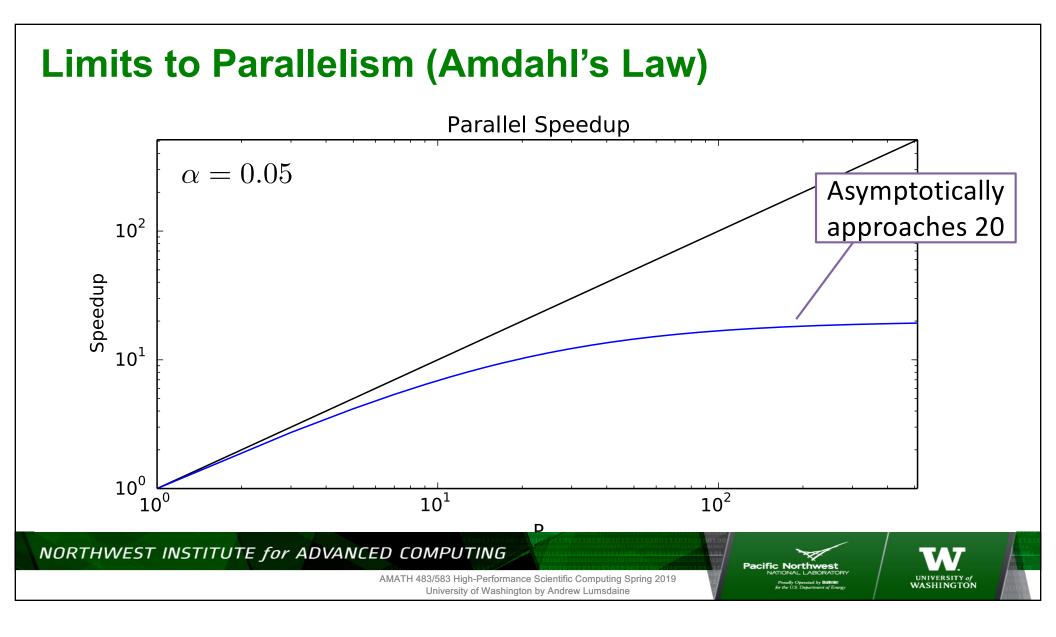


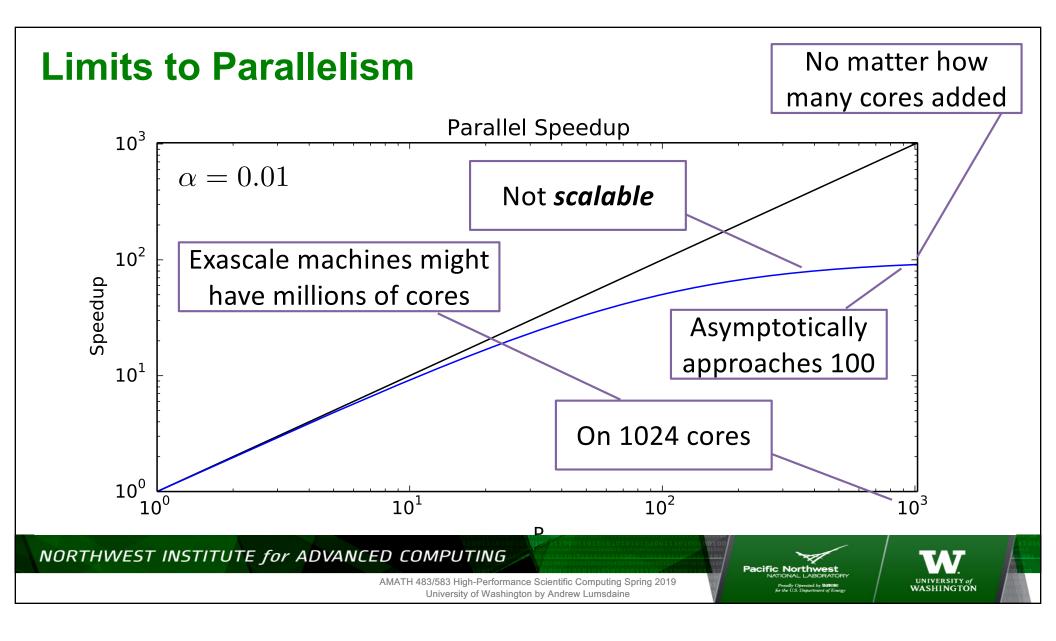






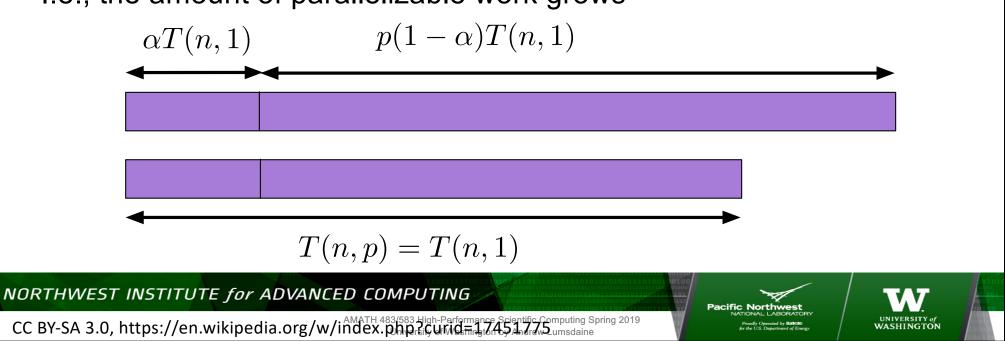


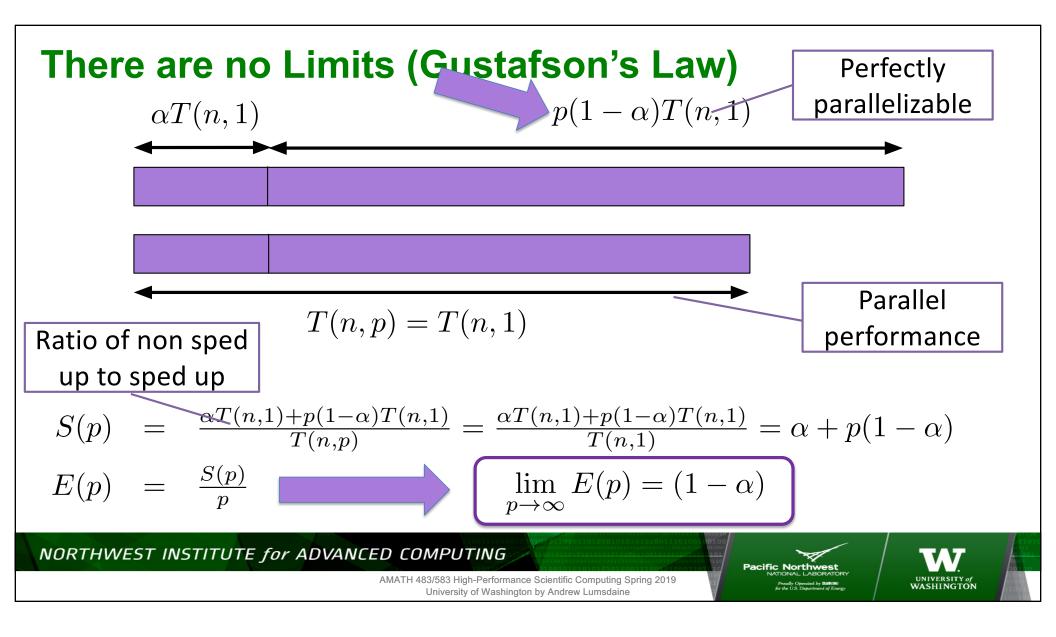


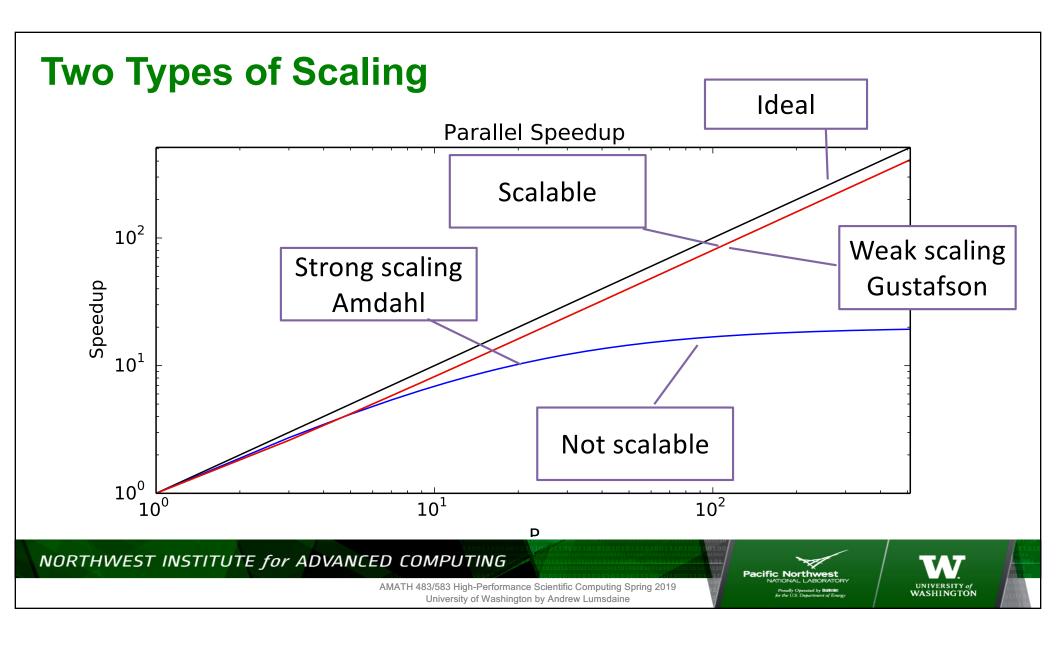


There are no Limits (Gustafson's Law)

- Doing the same problem faster and faster is not how we use parallel computers
- Rather, we solve bigger and more difficult problems
- I.e., the amount of parallelizable work grows







Stay Tuned

- C++ threads
- C++ async()
- C++ atomics

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